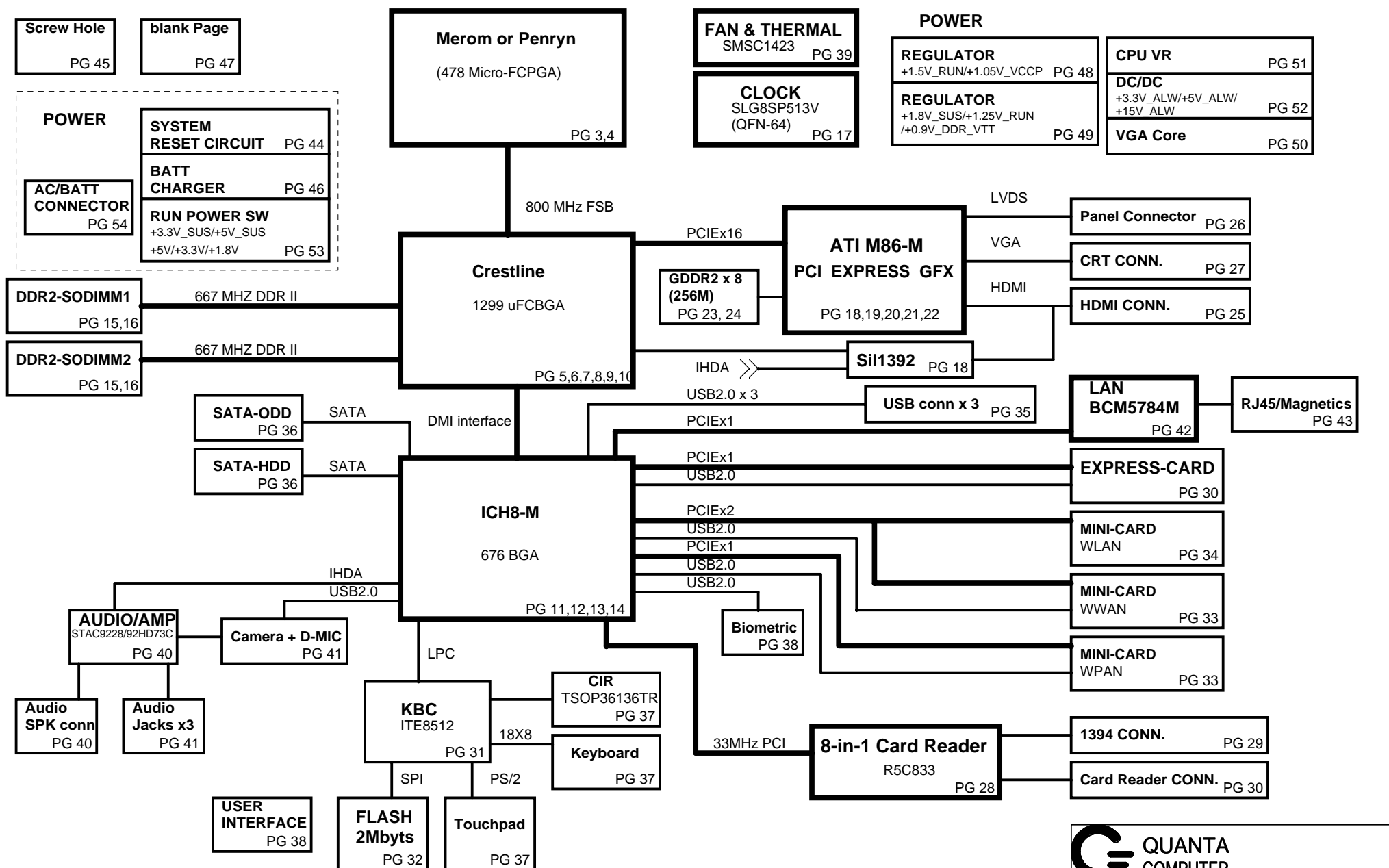


GM3(B) Pacino Intel Discrete & UMA Block Diagram

VER : 3A










Title		
Schematic Block Diagram1		
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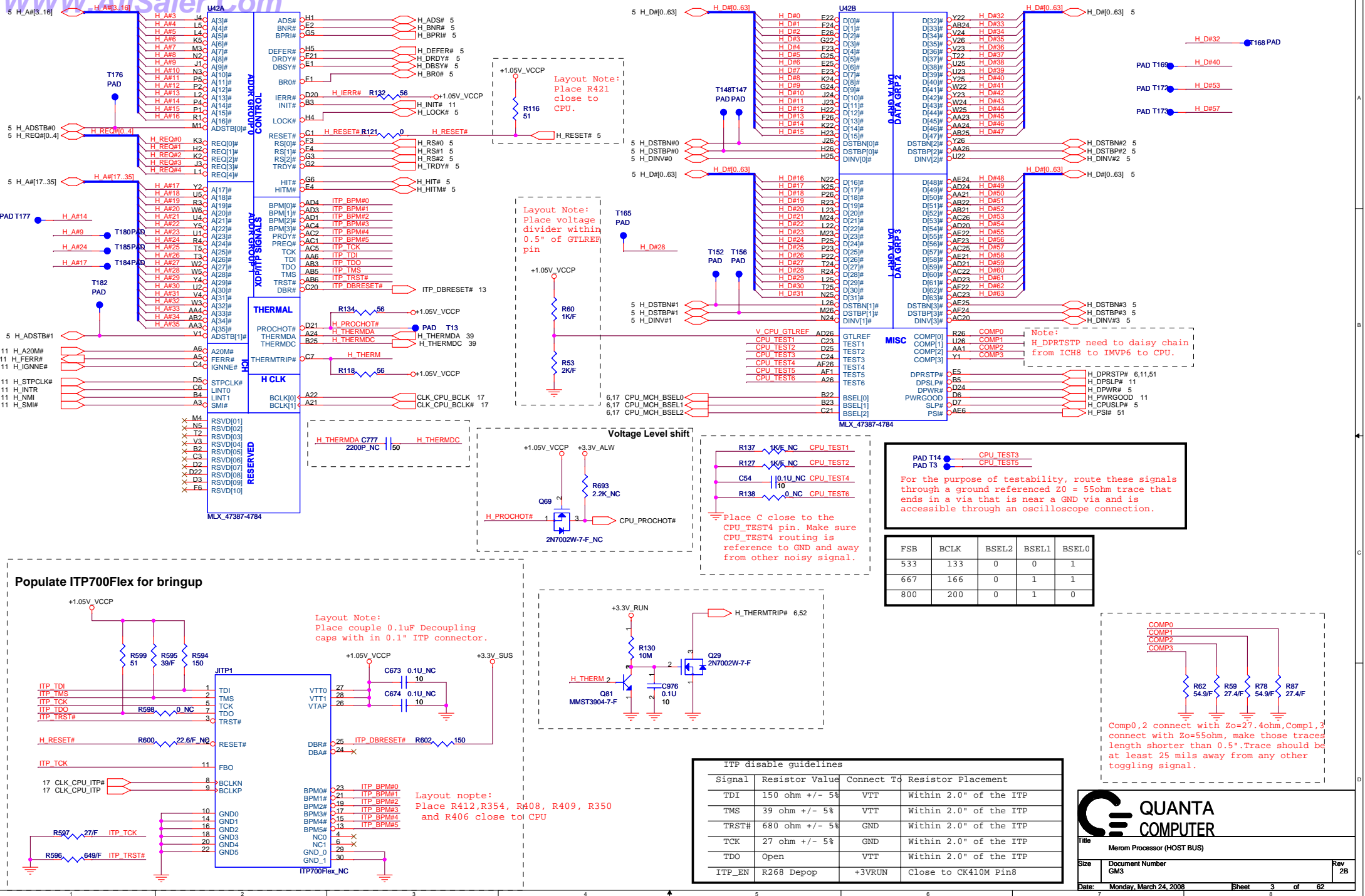
PAGE	DESCRIPTION
1	Schematic Block Diagram
2	Front Page
3-4	Merom
5-10	Crestline
11-14	ICH8M
15-16	DDRII SO-DIMM(200P)
17	Clock Generator
18-24	VGA
25	HDMI
26	LCD connector
27	CRT
28	Card reader PCI interface
29	Card reader & 1394
30	Express card & card reader conn.
31	SIO
32	Flash/RTC
33	WWAN/WPAN
34	WLAN
35	USB port
36	SATA HDD & ODD
37	TP/KB/MB/CIR
38	switch/LED
39	FAN/Thermal
40-41	Audio/CONN.
42-43	Docking Conn/Q-Switch
44	System Reset Circuit
45-46	Screw hole & Charger
47	Blank page
48	1.05VCCP & 1.5VRUN
49	1.8VSUS & 0.9VTT
50	VGA power circuit
51	CPU_ISL6266 (2phase)
52	D/D ISL6237 3.3V/5V
53	RUN Power Switch
54	DCIN,Batt
55	EMI CAP
56	SMBUS BLOCK
57	Power statu & Block diagram

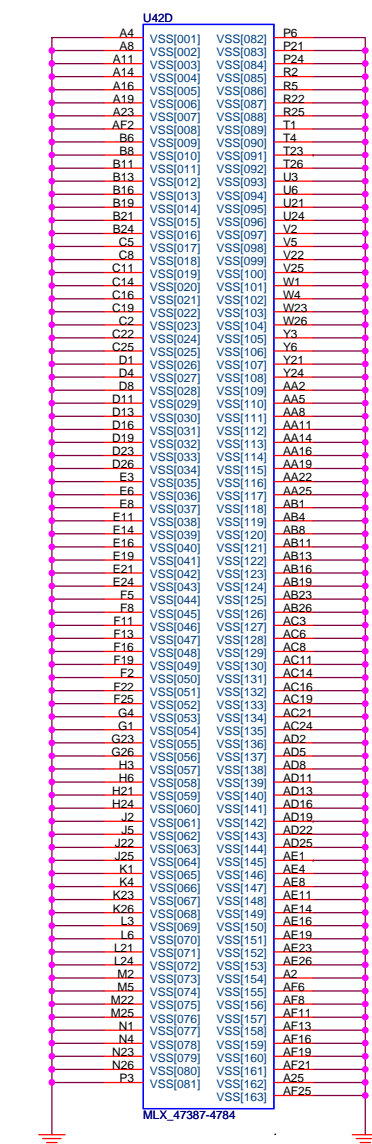
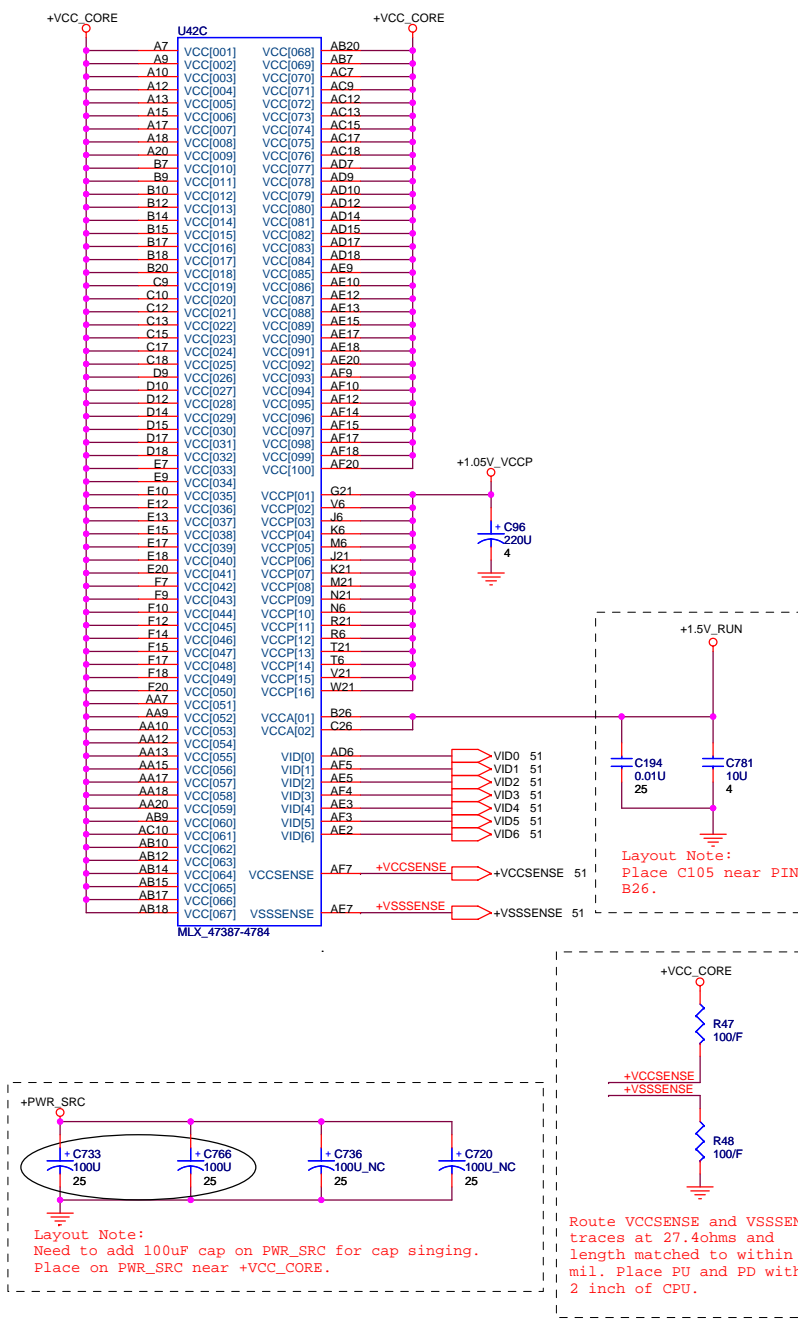
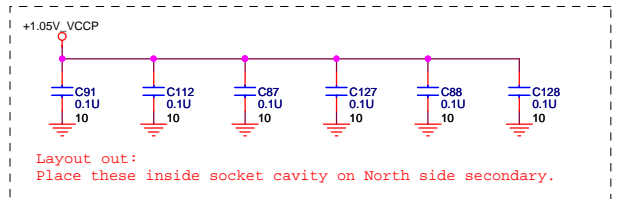
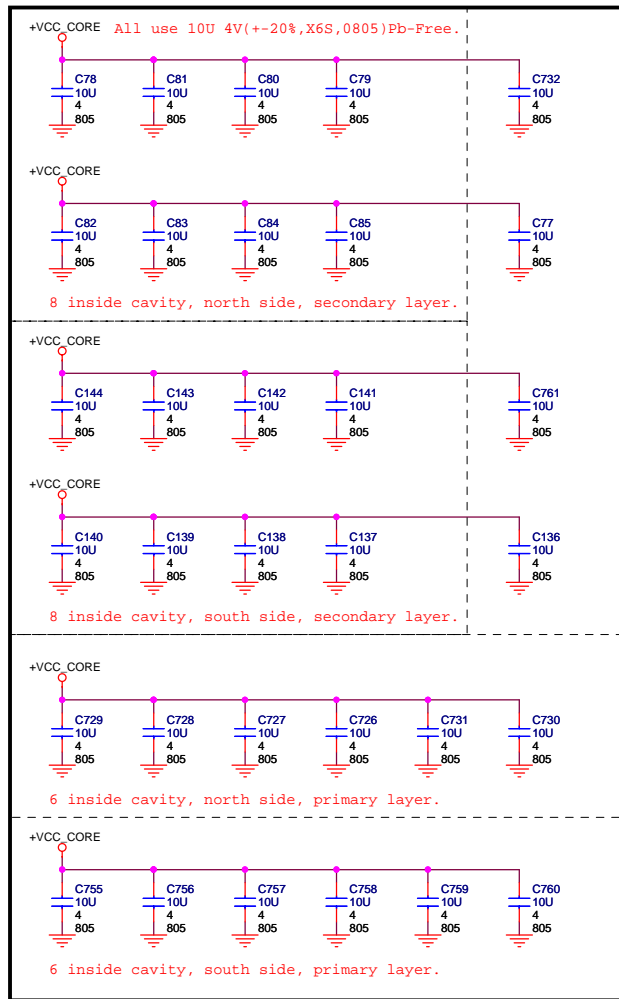
Power States

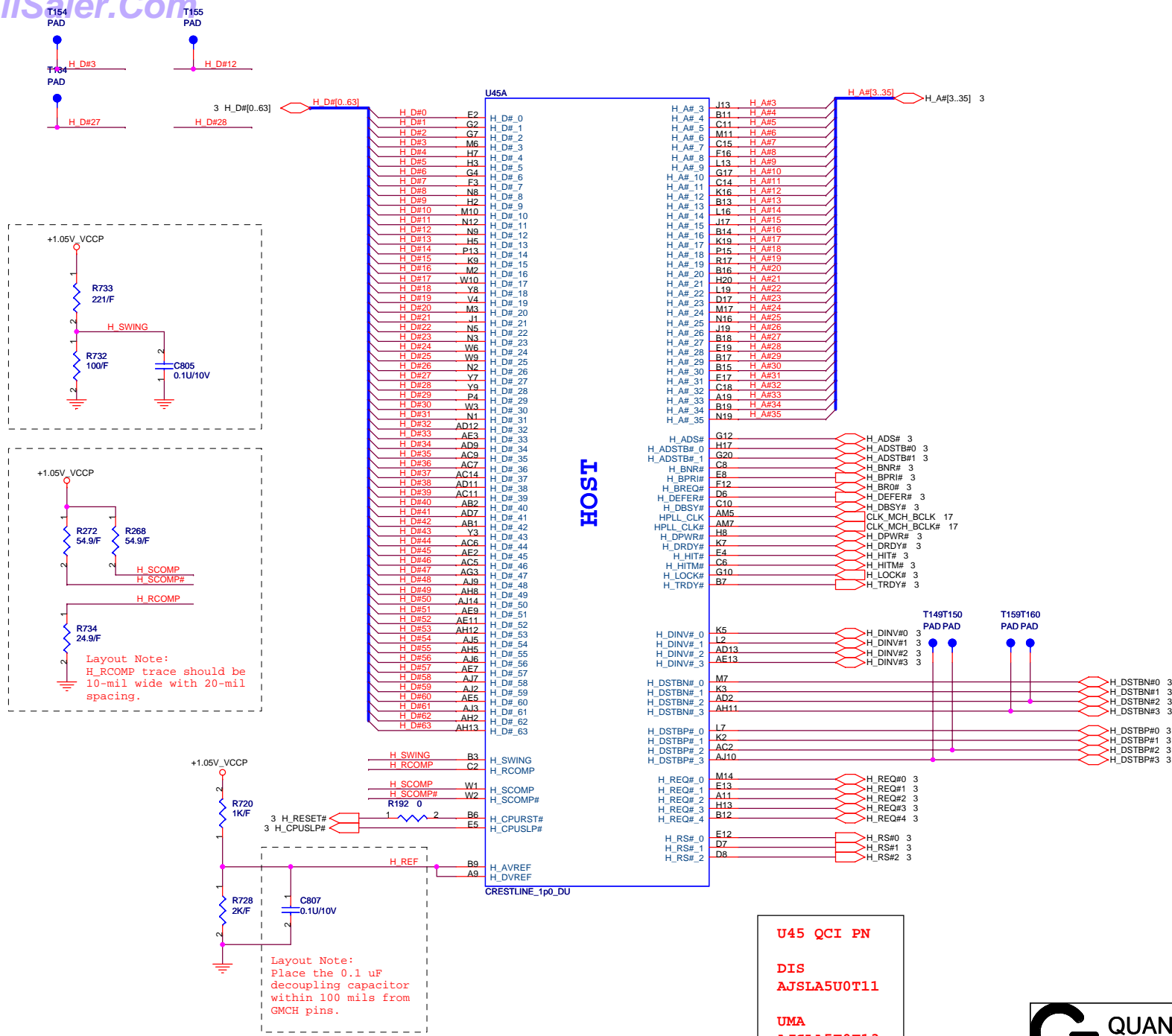
POWER PLANE	VOLTAGE	PAGE	DESCRIPTION	CONTROL SIGNAL	ACTIVE IN
+PWR_SRC	10V~+19V	4,26,32,34,48,49,50,51,52,55	MAIN POWER		S0~S5
+RTC_CELL	+3.0V~+3.3V	11,14,31,32	RTC		S0~S5
+3.3V_ALW	+3.3V	3,13,26,31,32,34,36,37,38,44,46,49,52,53,54	8051 POWER	ALWON	S0~S5
+5V_ALW	+5V	35,36,46,48,49,52,53,54	LCD/CHARGE POWER	ALWON	S0~S5
+15V_ALW	+15V	26,36,37,52,53	LARGE POWER	+5V_ALW	S0~S5
+3.3V_LAN	+3.3V	42,43	LAN POWER	AUX_ON	
+5V_SUS	+5V	14,38,50,51,53	SLP_S5# CTRLD POWER	SUS_ON	
+3.3V_SUS	+3.3V	3,11,12,13,14,20,30,37,38,43,48,49,50,51,53	SLP_S5# CTRLD POWER	3.3V_SUS_ON	
+1.8V_SUS	+1.8V	6,8,9,15,48,49,50,53,55	SODIMM POWER	DDR_ON	
+0.9V_DDR_VTT	+0.9V	16,49,53	SODIMM POWER	0.9V_DDR_VTT_ON	
+5V_RUN	+5V	14,20,25,27,36,37,38,39,40,41,53	SLP_S3# CTRLD POWER	RUN_ON	
+3.3V_RUN	+3.3V	6,8,9,11,12,13,14,15,17,19,20,22,25,26,27,28,30,33,34,36,38,39,40,41,42,53,55	SLP_S3# CTRLD POWER	3.3V_RUN_ON	
+1.8V_RUN	+1.8V	19,20,21,22,23,24,25,38,53	SDVO POWER	RUN_ON	
+1.5V_RUN	+1.5V	4,9,14,30,33,34,48,,53,55	CALISTOGA/ICH8 POWER	1.5V_RUN_ON	
+1.25V_RUN	+1.25V	6,9,14,49,53	CALISTOGA/ICH8 POWER	1.25V_RUN_ON	
+1.05V_VCCP	+1.05V	3,4,5,6,8,9,11,14,37,48,55	CPU/CALISTOGA/ICH8 POWER	1.05V_RUN_ON	
+VCC_CORE	+0.7V~+1.5V	4,51	CPU CORE POWER	IMVP_VR_ON	
+LCDVCC	+3.3V	26	LCD Power	LCDVCC_TST_EN & ENVDD	
+5V_MOD	+5V	36	Module Power	MODC_EN#	
+5V_HDD	+5V	36	HDD Power	HDDC_EN#	
+5V_ALW2	+5V	37,38,52,53	LED power source	LDO output	

GND PLANE	PAGE	DESCRIPTION
 8731AGND	46	
 AGND_0.9V	49	
 AGND_DC/DC	52	
 AGND_DC2	48	
 AGND_DDR	49	
 AGND_ISL6260	51	
 GND	ALL	

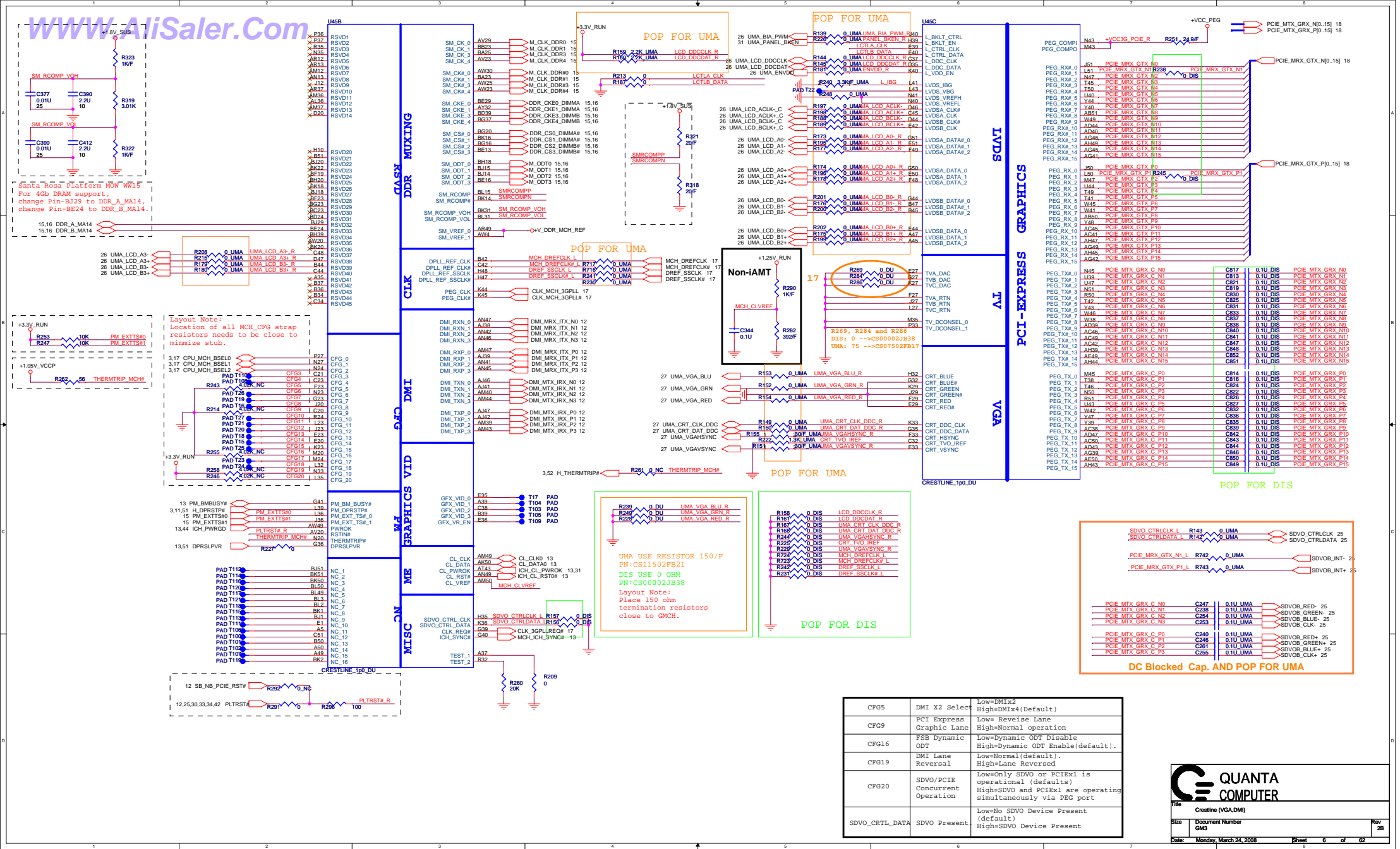
 QUANTA COMPUTER		
Title Index & Power Status		
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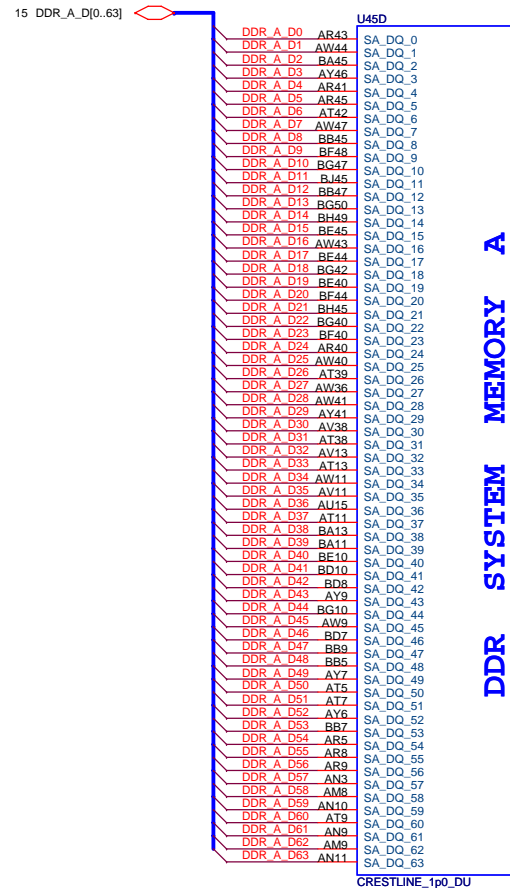
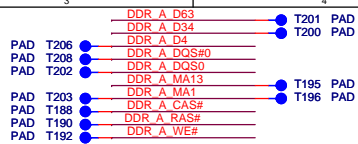




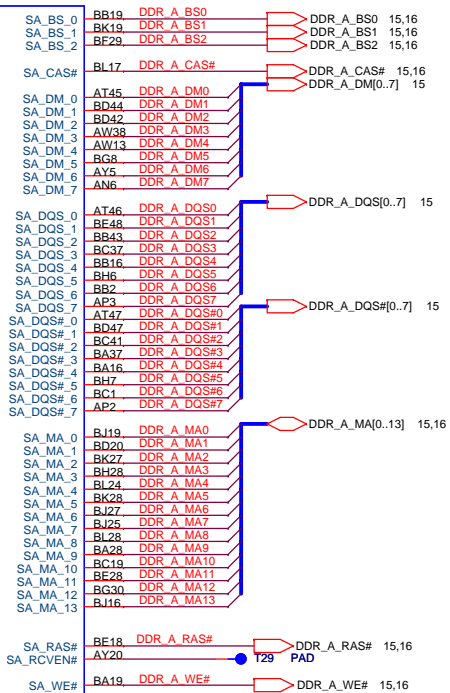


Title Crestline (HOST)		
Size GM3	Document Number	Rev 2B
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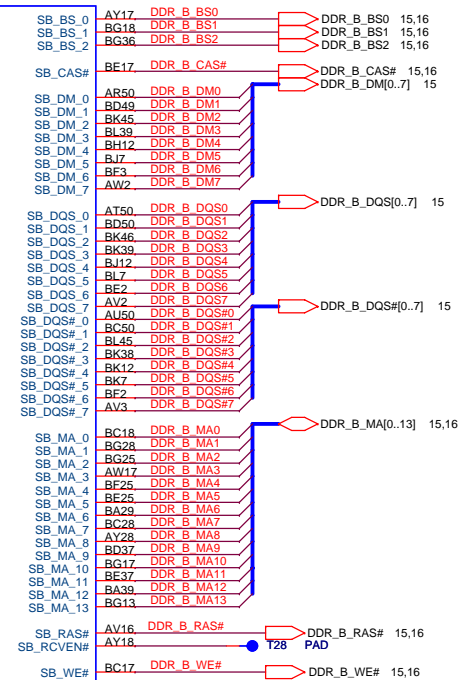




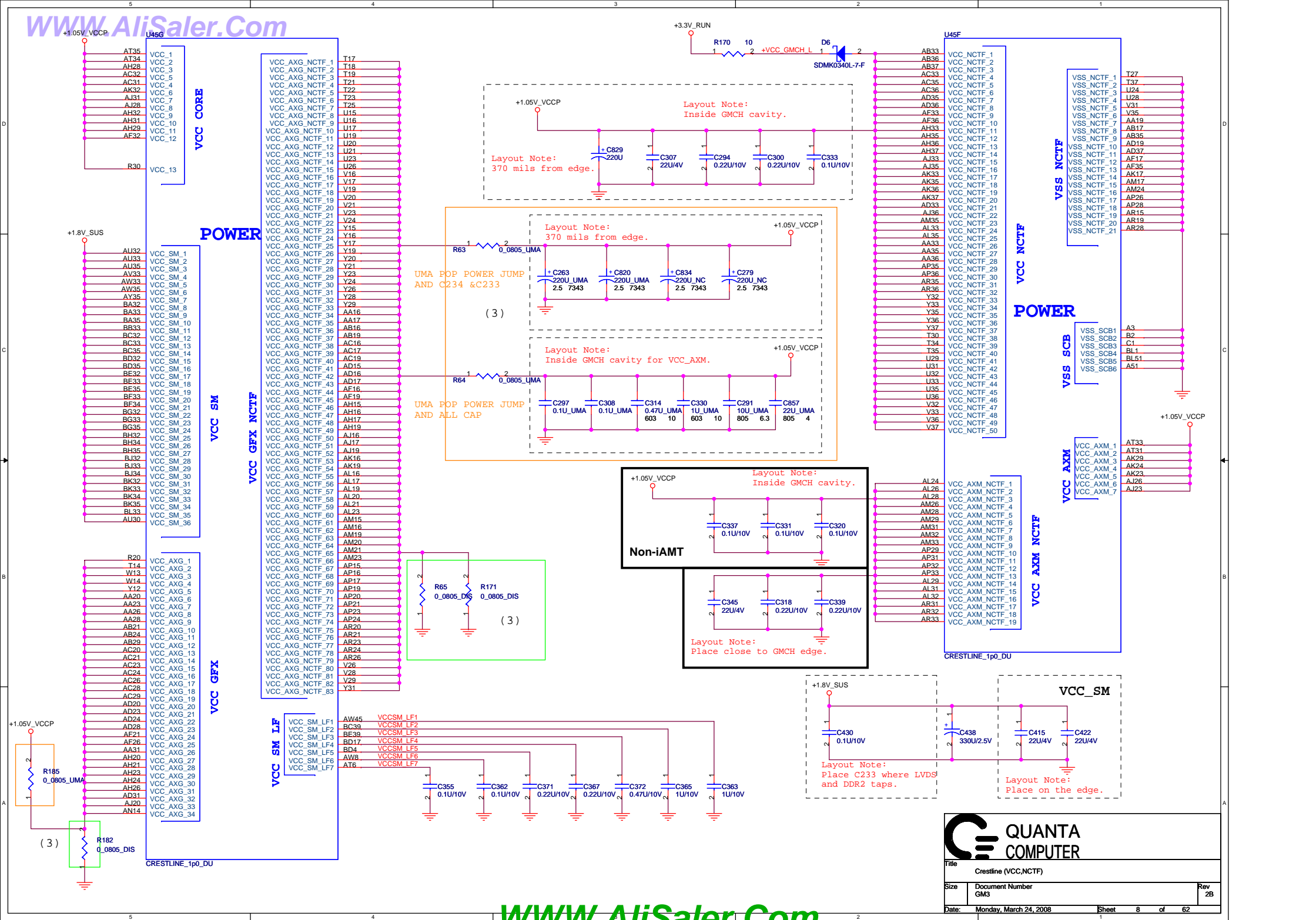
DDR SYSTEM MEMORY A

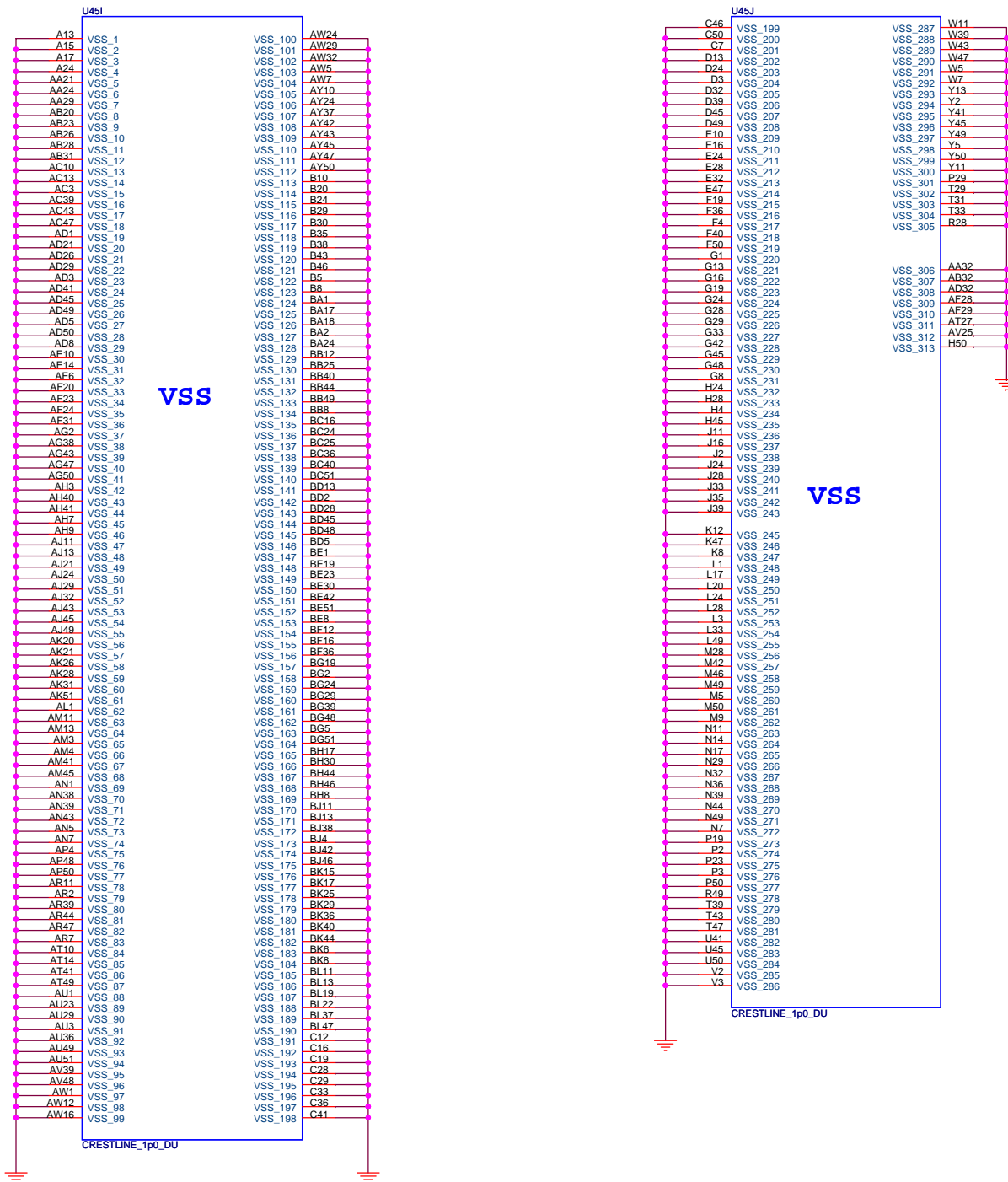


DDR SYSTEM MEMORY B

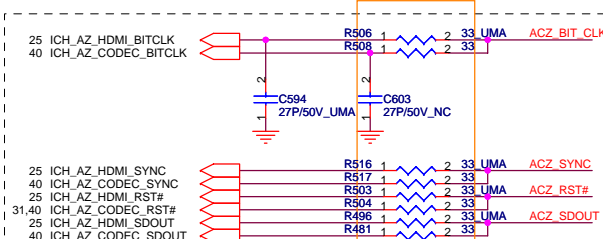
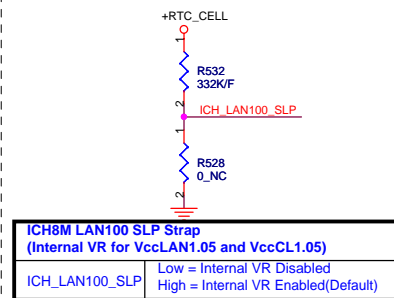
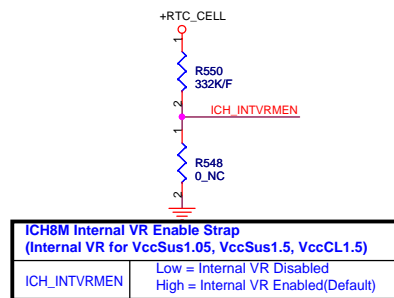
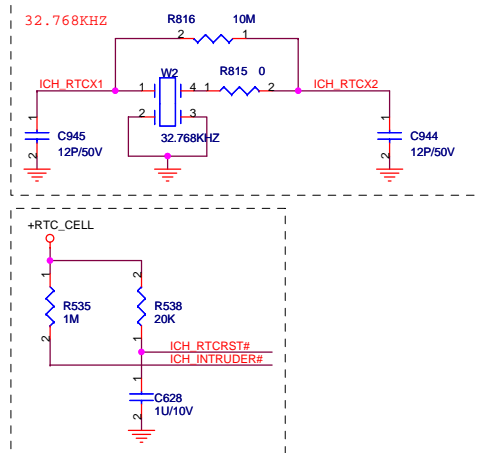


Title		
Crestline (DDR2)		
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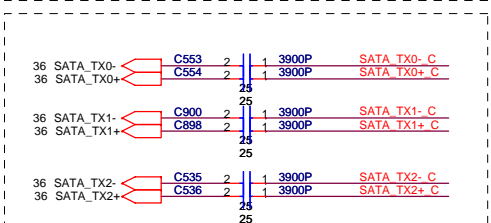




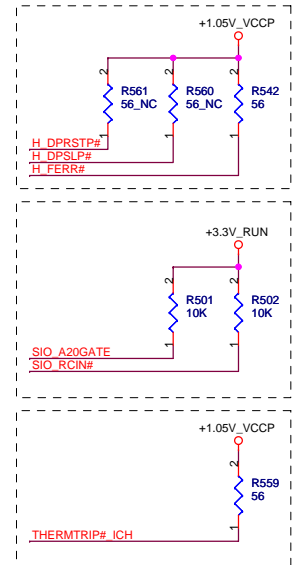
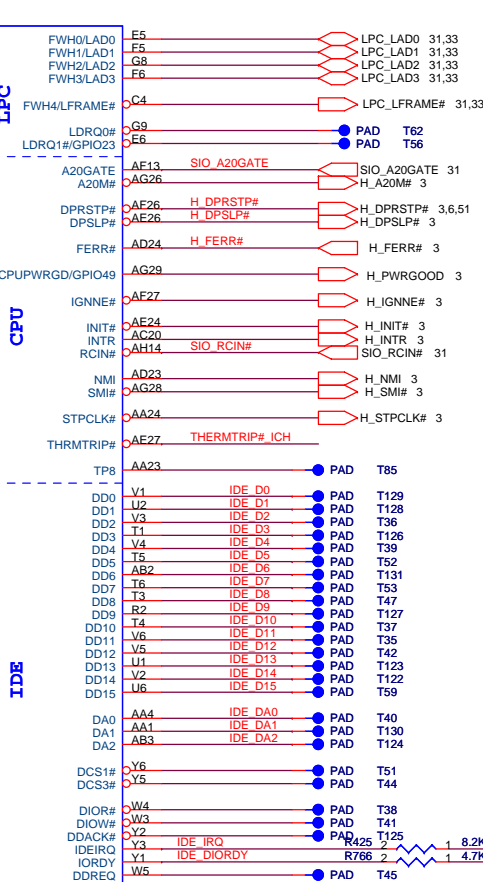
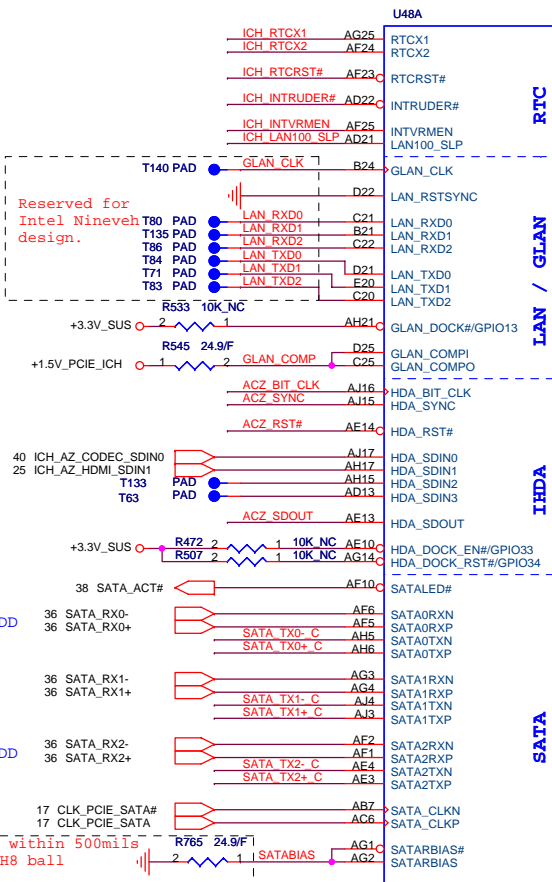
Title			Crestline (VSS)
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Place all series terms close to ICH8 except for SDIN input lines, which should be close to source. Placement of R603, R600, R607 & R612 should equal distance to the T split trace point as R604, R599, R606 & R608 respectively. Basically, keep the same distance from T for all series termination resistors.



Distance between the ICH-8 M and cap on the "P" signal should be identical distance between the ICH-8 M and cap on the "N" signal for same pair.



XOR Chain Entrance Strap		
ICH_RSVD	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation (Default)
1	1	Set PCIE port config bit 1

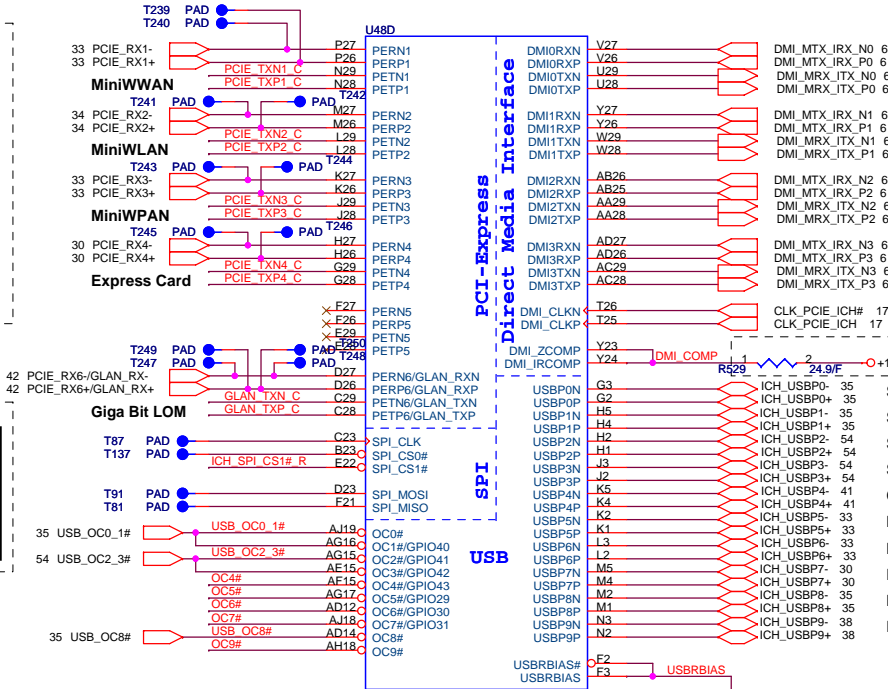
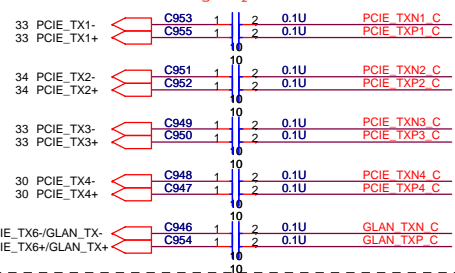
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Title: ICH8-M (CPU,IDE,SATA,LPC,AC97,LAN)

Size: Document Number: GM3 Rev: 2B

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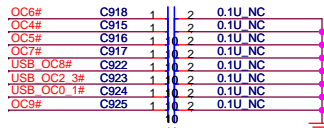
Place TX DC blocking caps close ICH8.



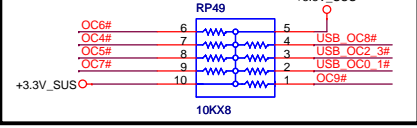
Place within 500mils of ICH8

		GNT0#	SPI_CS1#
LPC	11	No stuff	No stuff
PCI	10	No stuff	No stuff
SPI	01	Stuff	No stuff

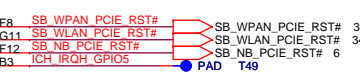
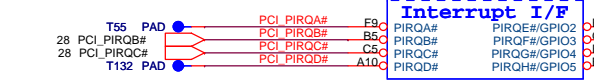
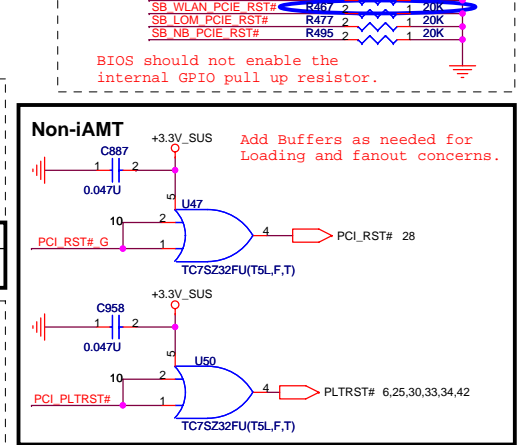
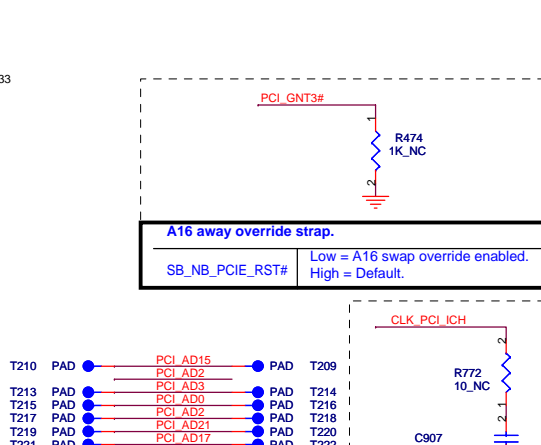
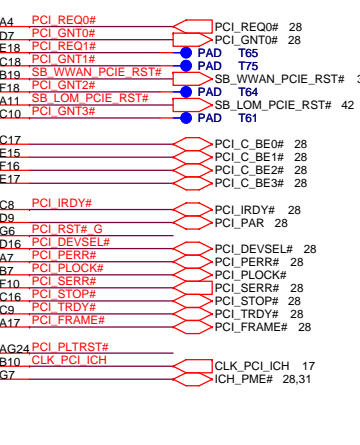
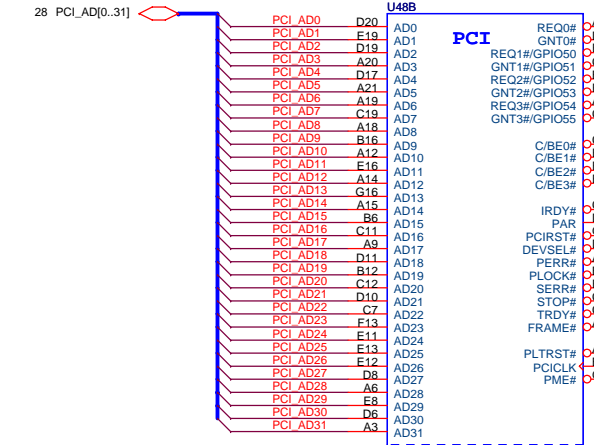
WWAN Noise - ICH improvements



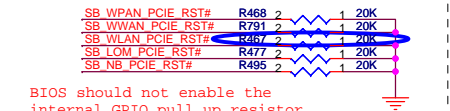
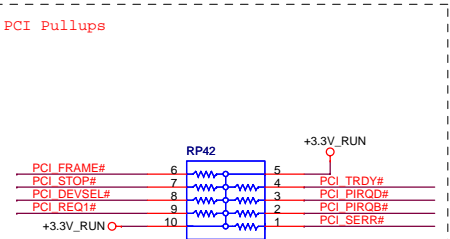
Non-iAMT



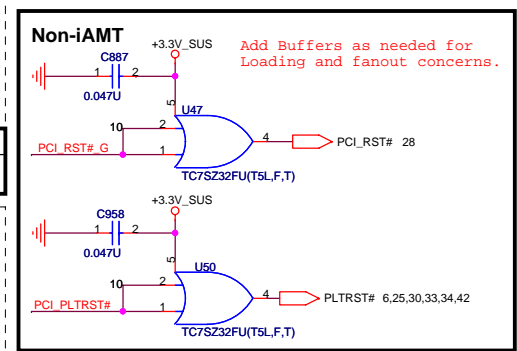
Short F2 and F3 at the package and keep length to less than 500mils. Trace Impedance should be 60ohms +/- 15%.



- Side pair Top / left
- Side pair bottom / left
- Side pair top/right(DB)
- Side pair Bot right(DB)
- Camera
- Mini Card (WWAN)
- Mini Card (WPAN)
- Express Card
- left side signal USB port
- Biometric

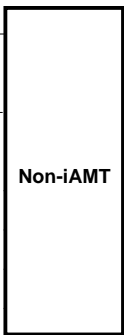
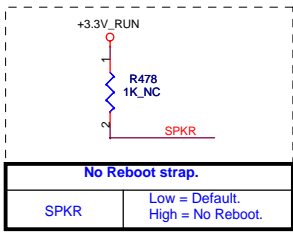
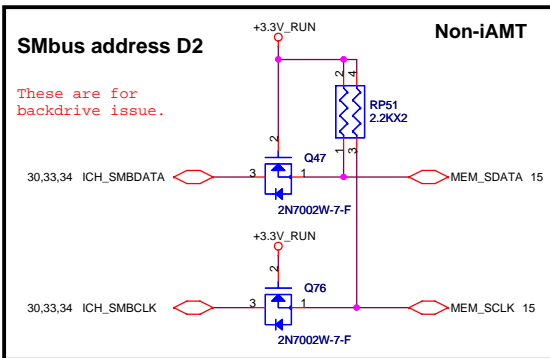
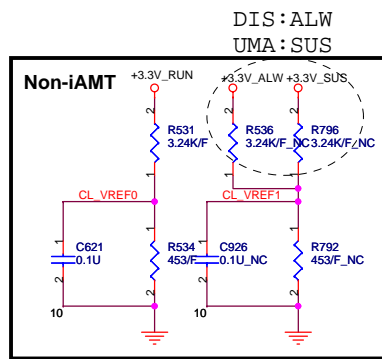
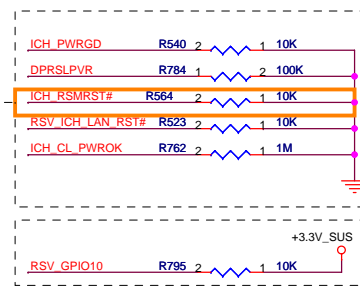
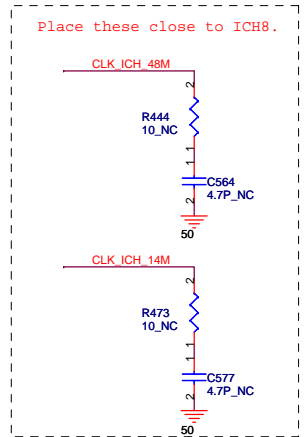
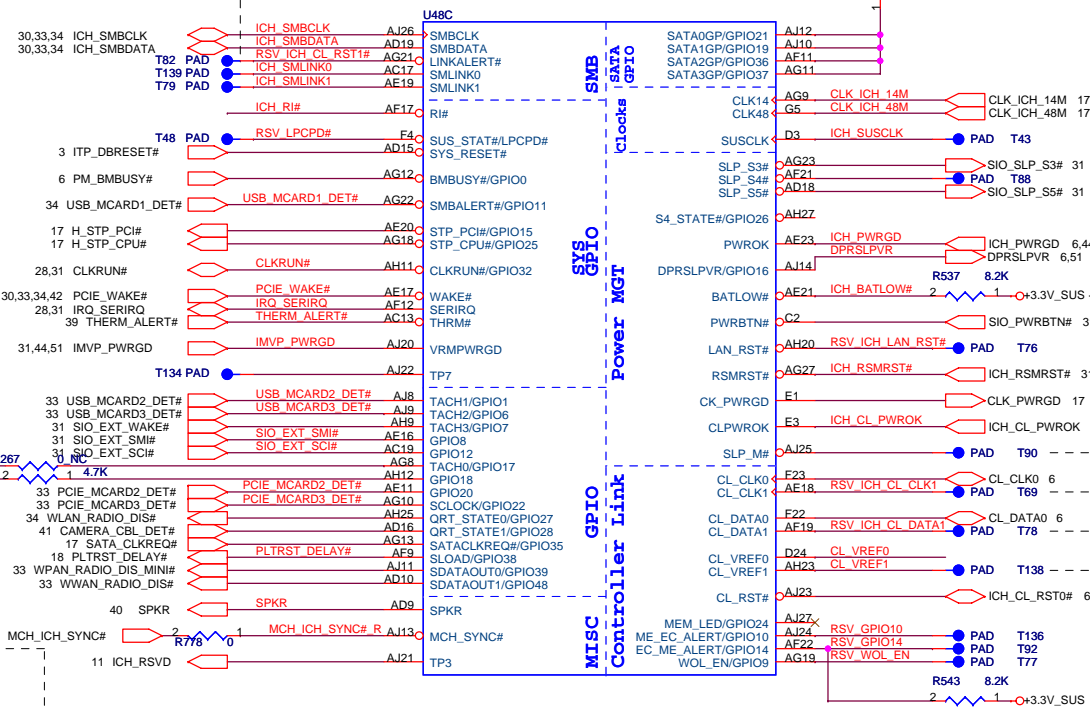
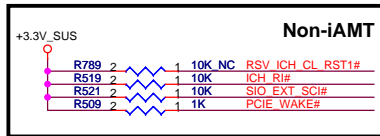
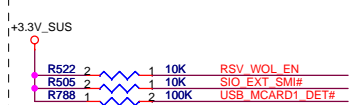
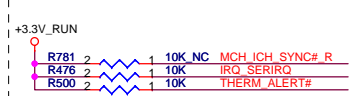
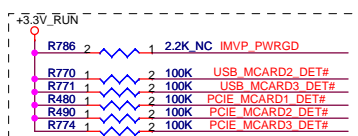
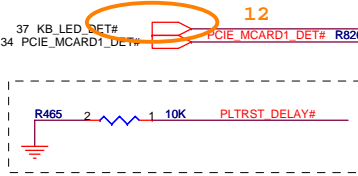
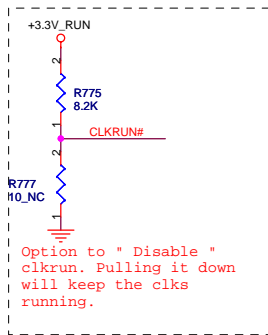
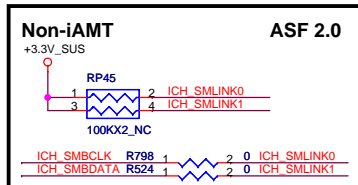
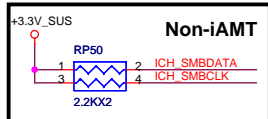


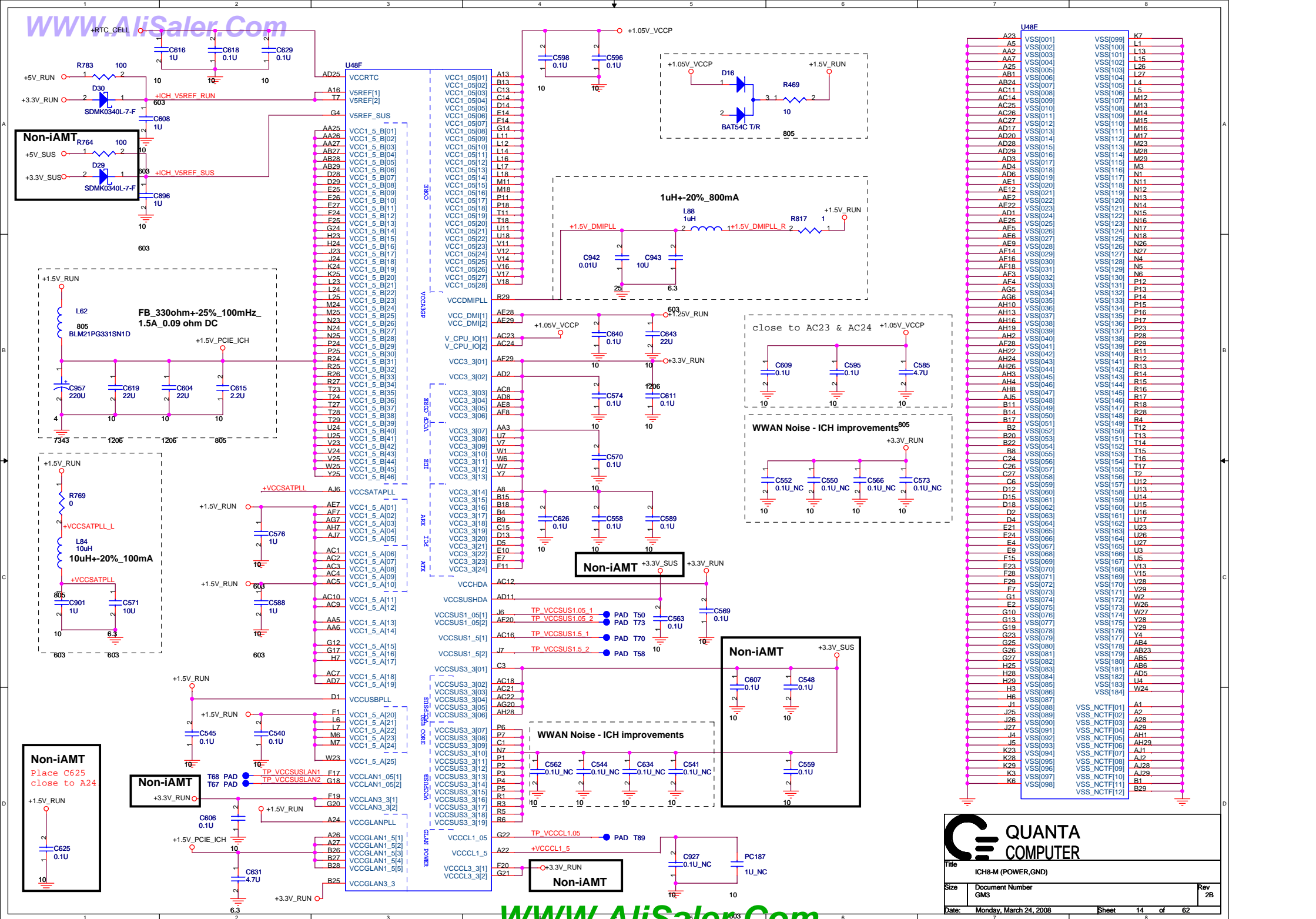
BIOS should not enable the internal GPIO pull up resistor.



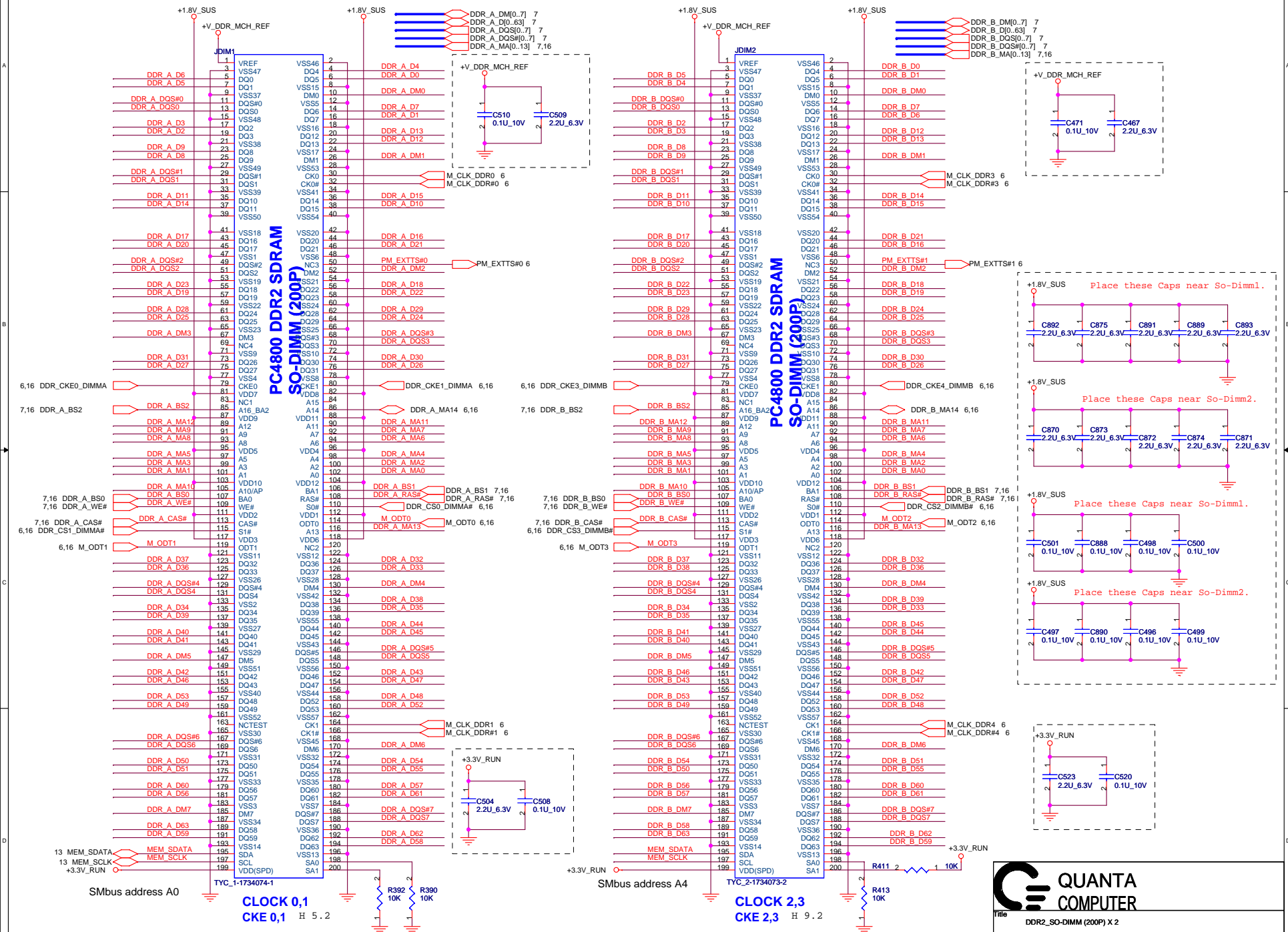
QUANTA
COMPUTER

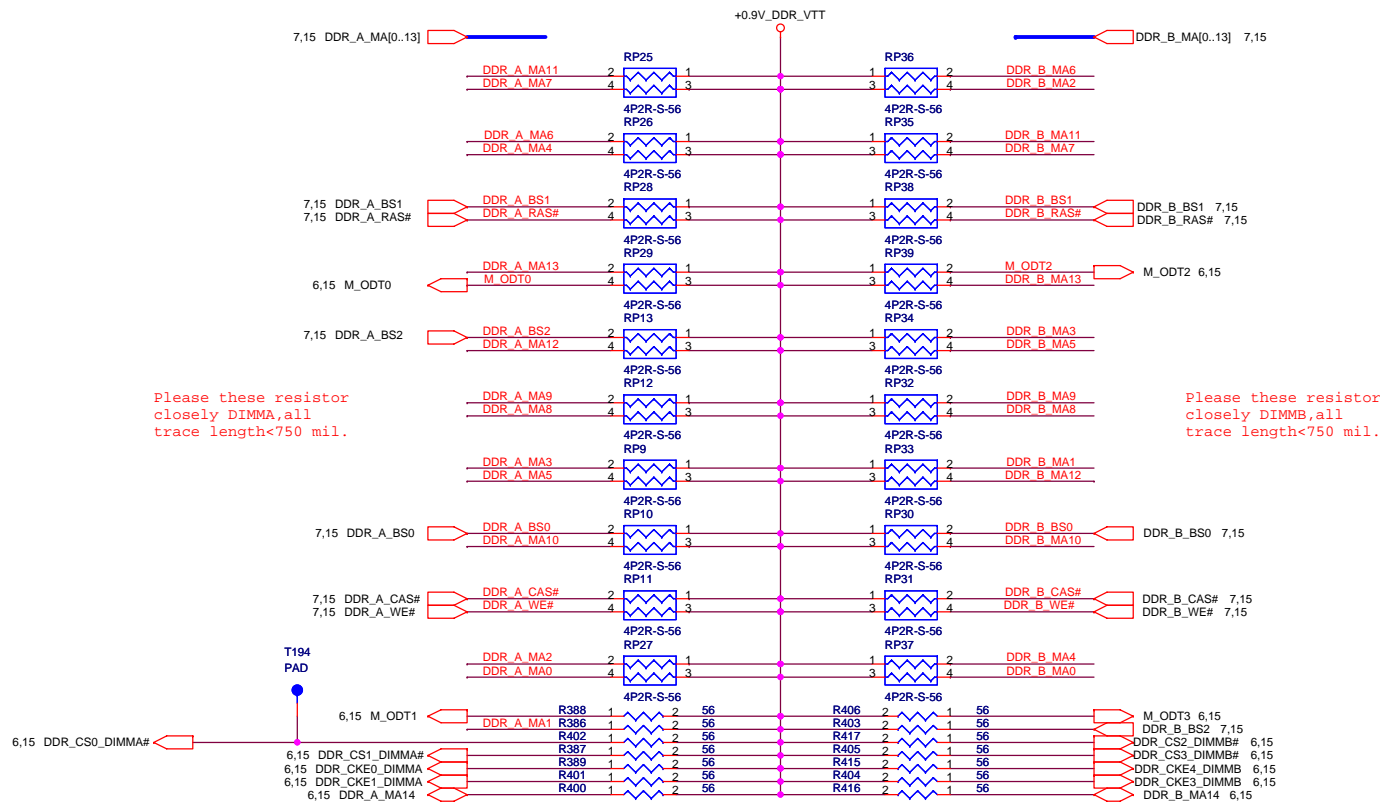
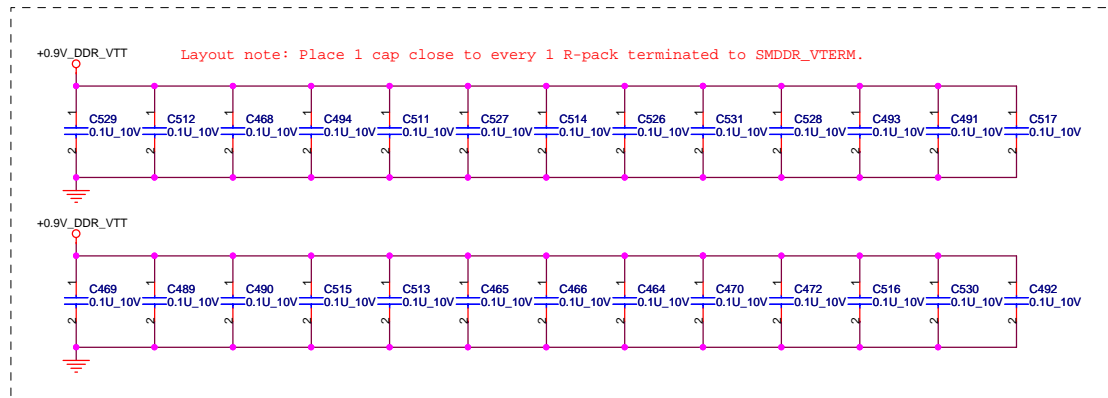
Title ICH8-M (USB,DMI,PCIE,PCI)		
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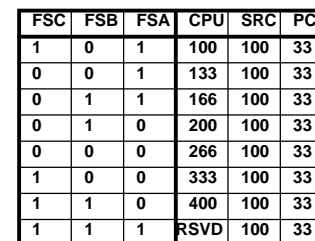
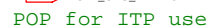
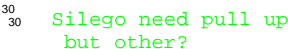
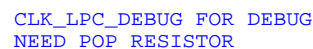
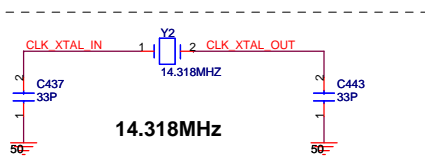


SLAVE





Title DDR2 RES. ARRAY		
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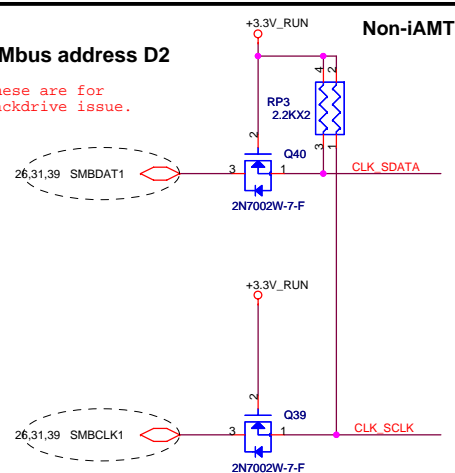
FSC	FSB	FSA	CPU	SRC	PO
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

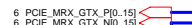


27M_SEL (PIN13)	PIN20	PIN21	PIN24	PIN25
0=UMA	DOT96T	DOT96C	96/ 100M_T	96/ 100M_C
1 = Disc. GRFX down	SRCT0	SRCC0	27Mout	27MSSout

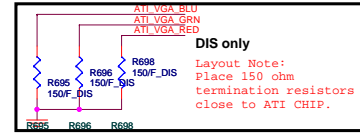
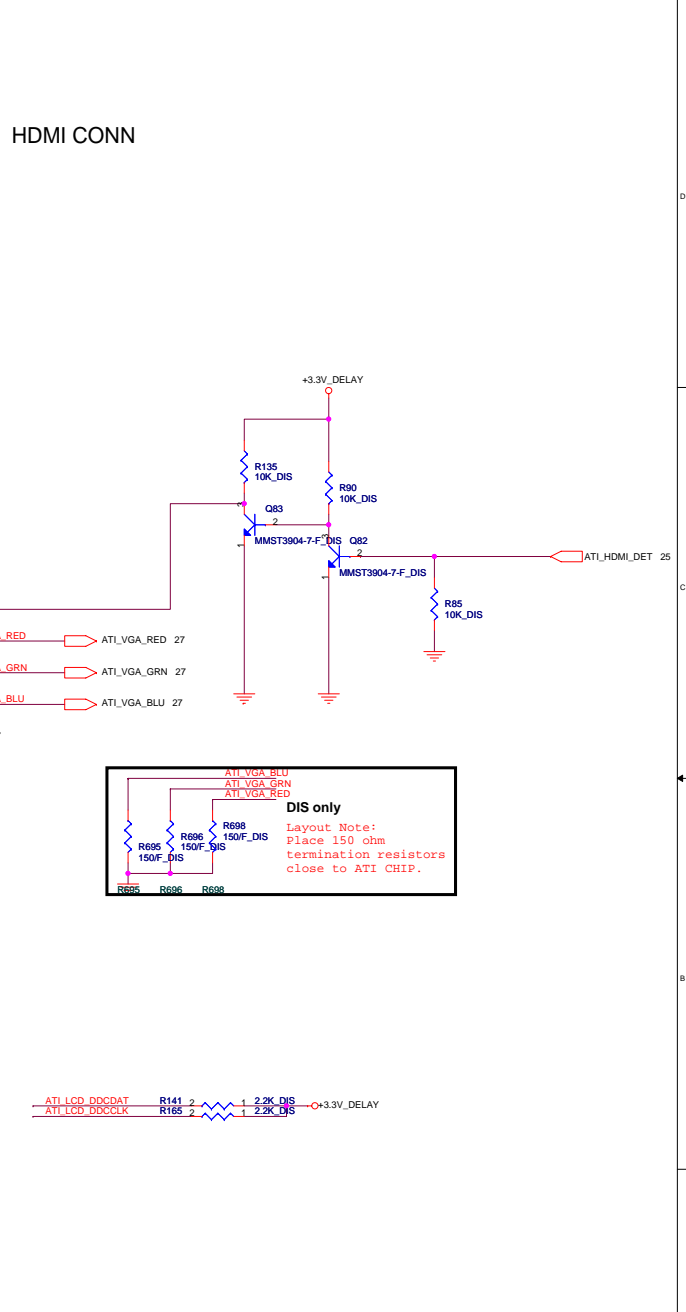
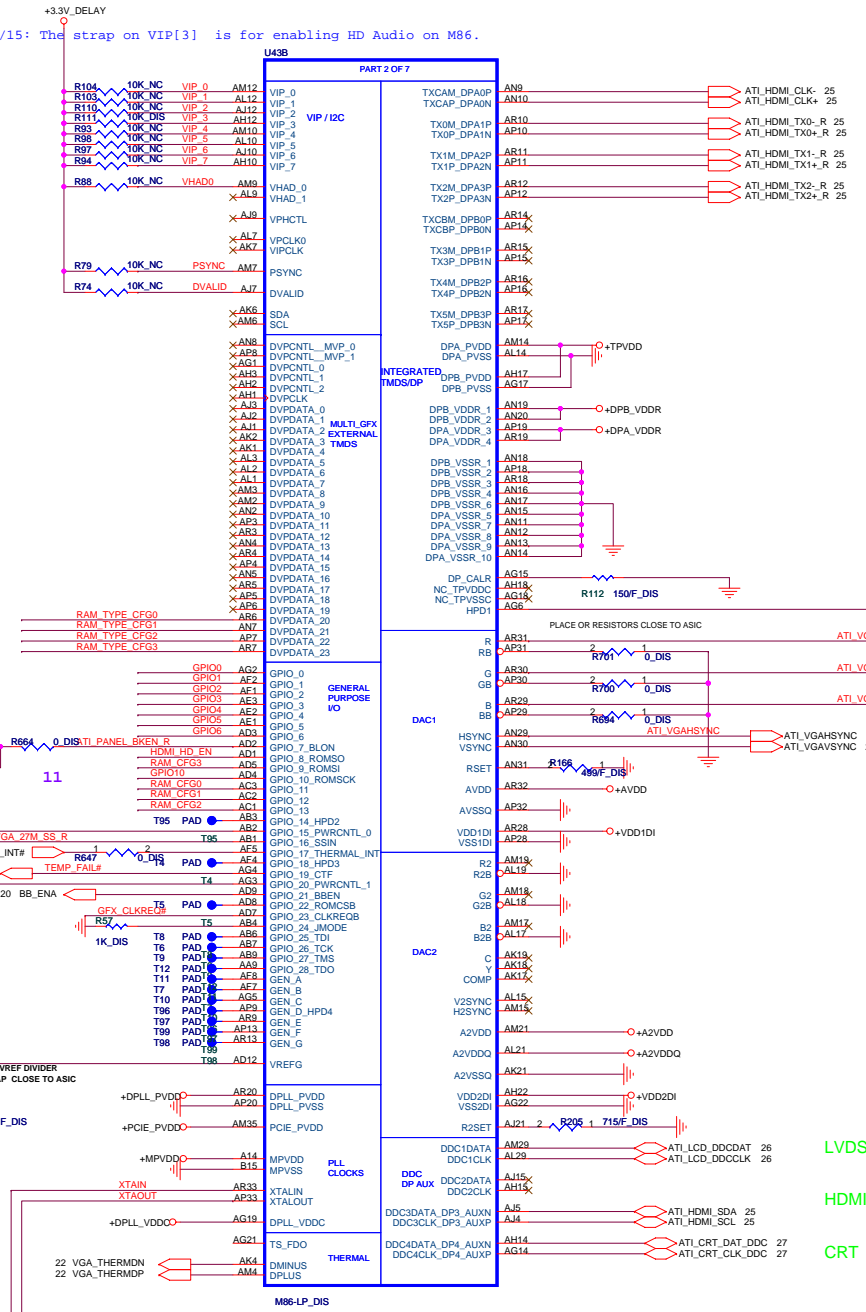
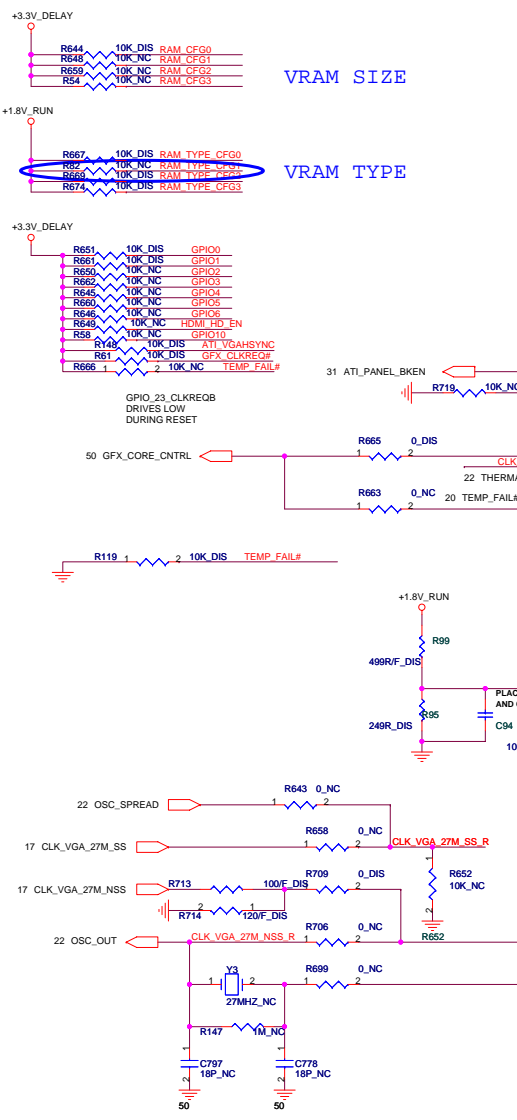


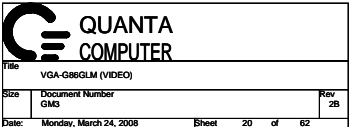
These are for
backdrive issue.



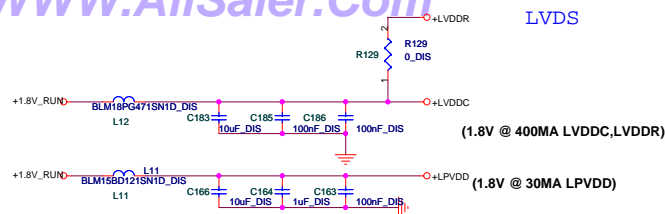


Memory Straps	RAM_TYPE_CFG3	RAM_TYPE_CFG2	RAM_TYPE_CFG1	RAM_TYPE_CFG0
400 MHz 256MB(16M*16) Hynix	1	1	1	1
400 MHz 256MB(16M*16) Qimonda	1	1	1	0
500 MHz 256MB(16M*16) Hynix	1	1	0	1
500 MHz 256MB(16M*16) Qimonda	1	1	0	0
500 MHz 256MB(16M*16) Samsung	1	0	1	1

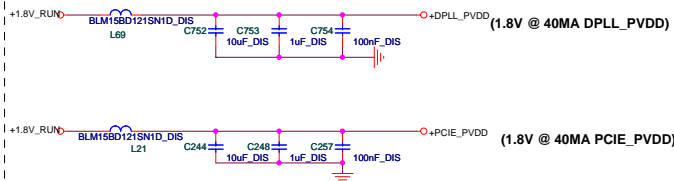




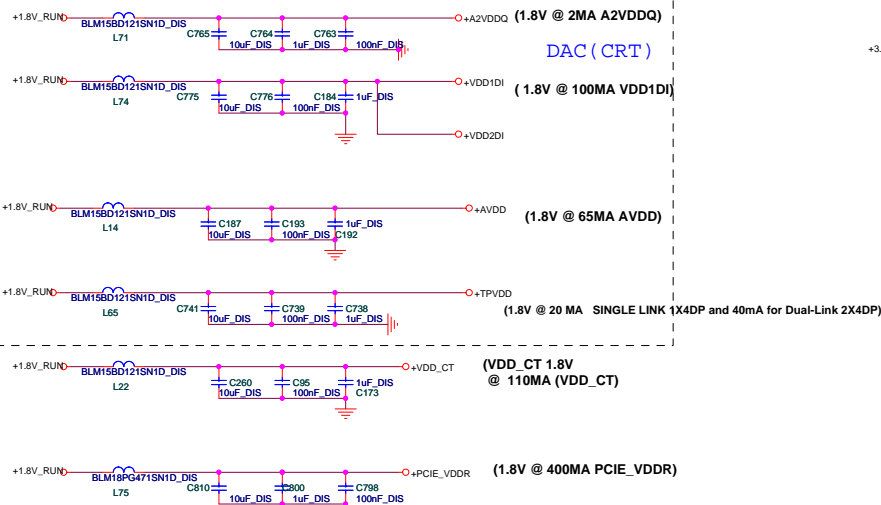
LVDS



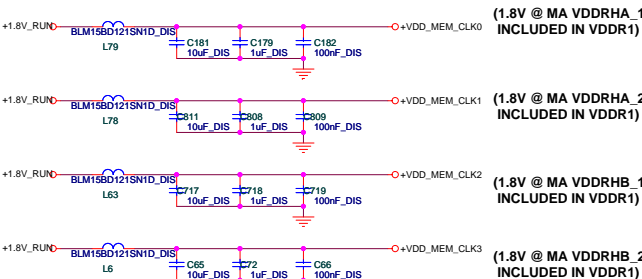
PLL_CLK



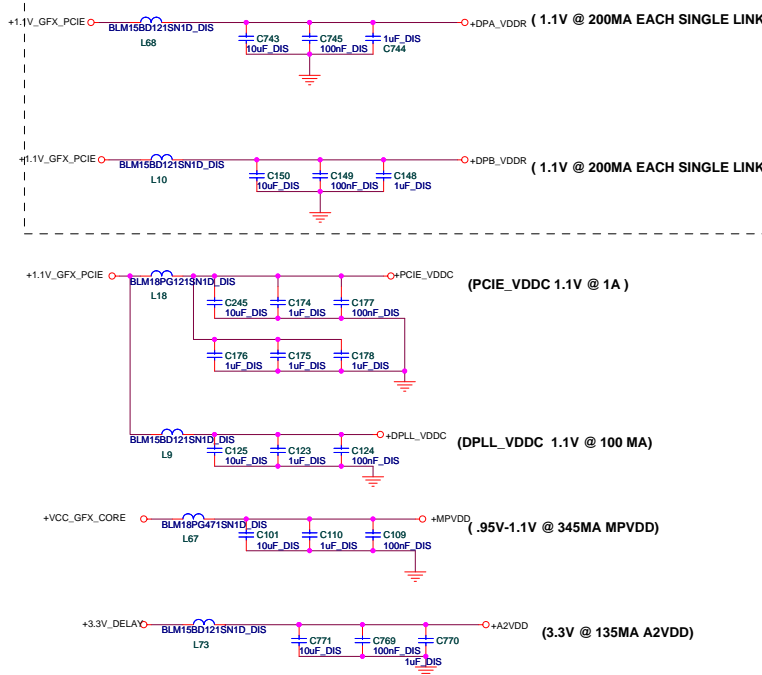
DAC (CRT)



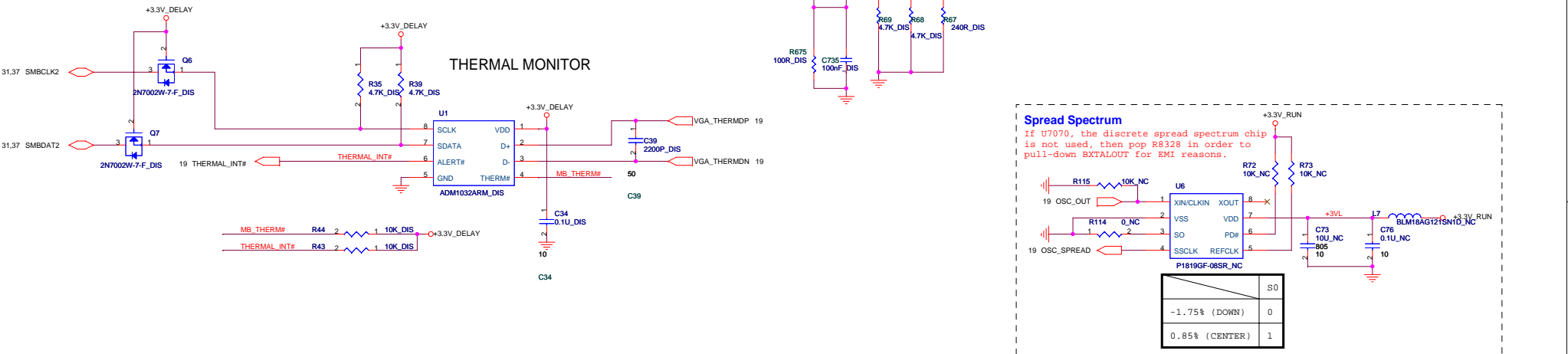
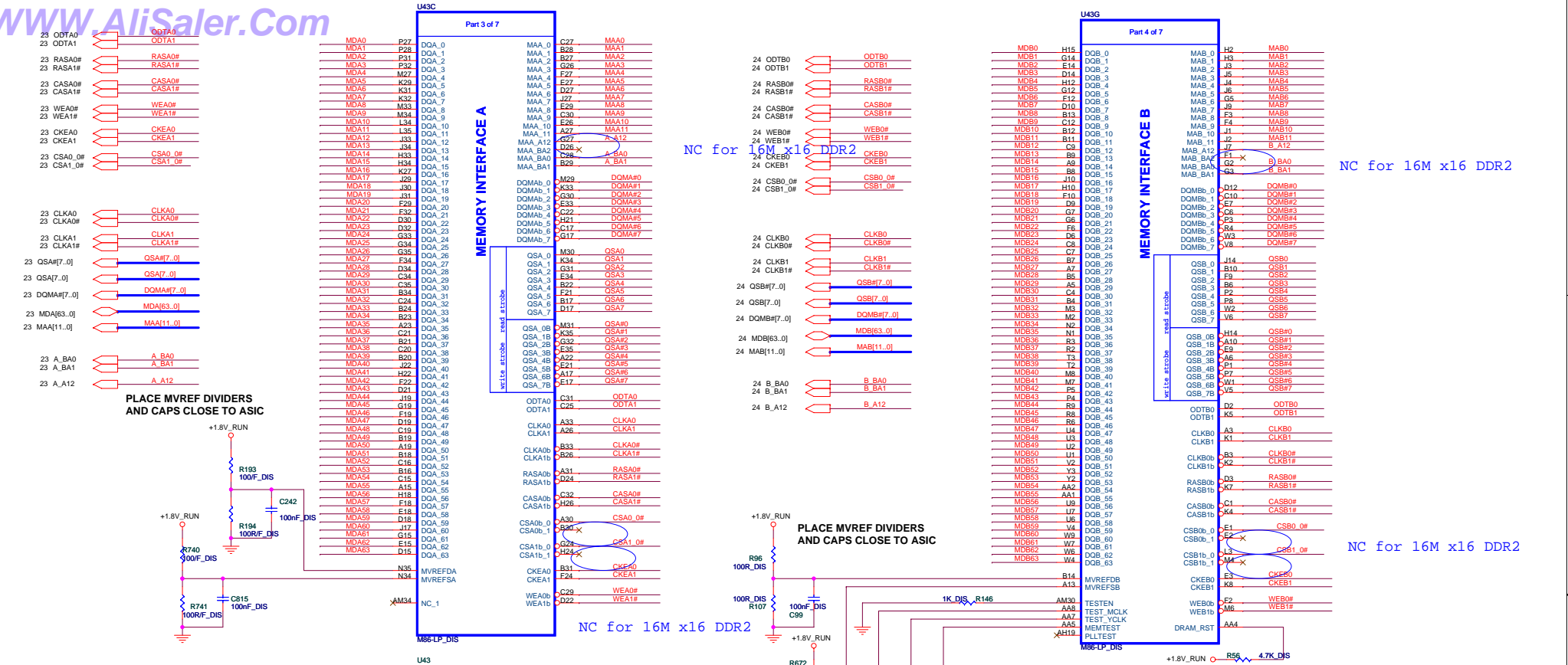
MEM IO CLK



TMDS

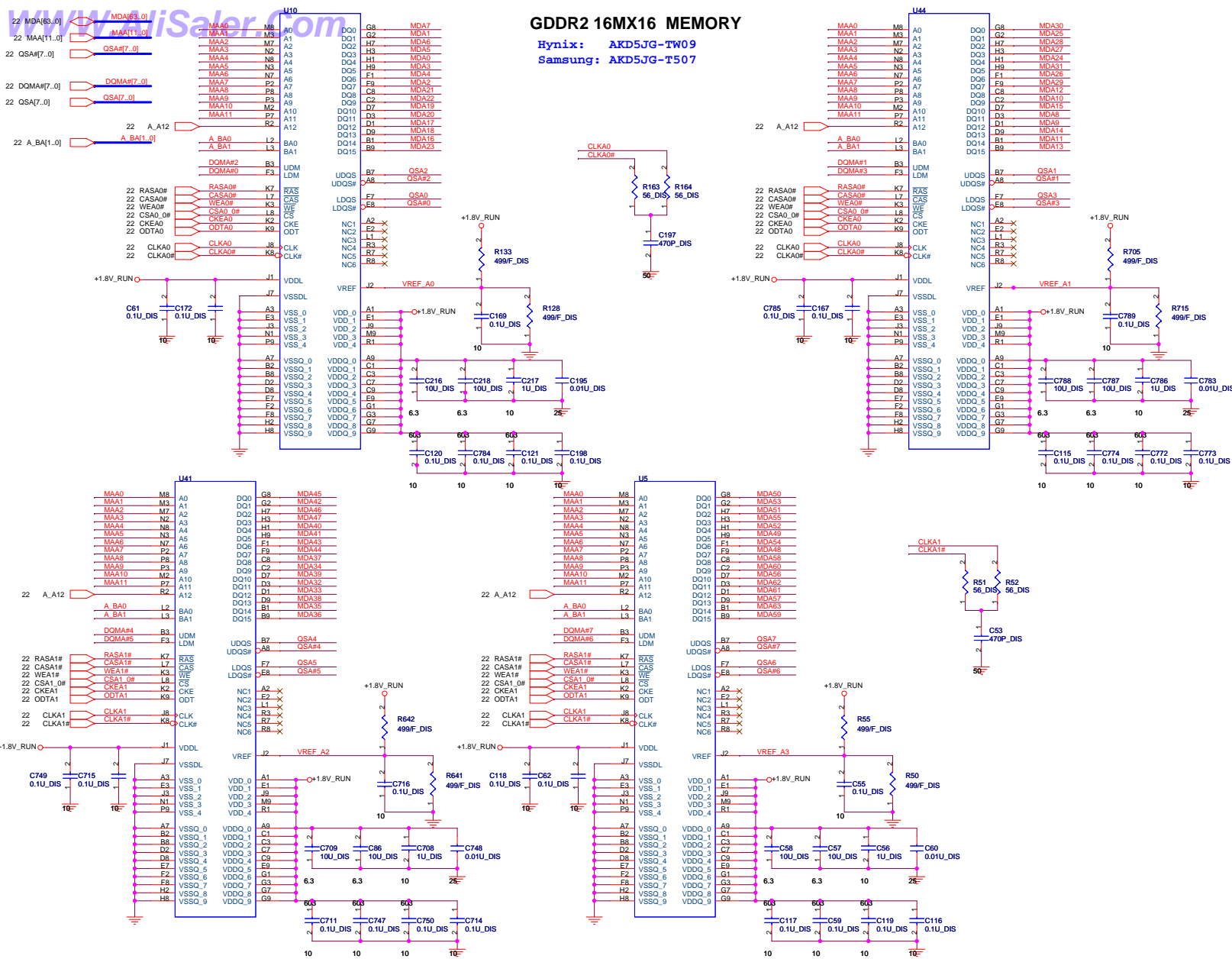


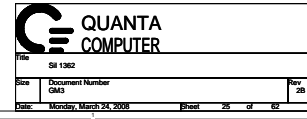
PLACE ALL DECOUPLING AS CLOSE TO ASIC AS POSSIBLE

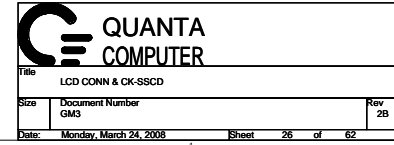


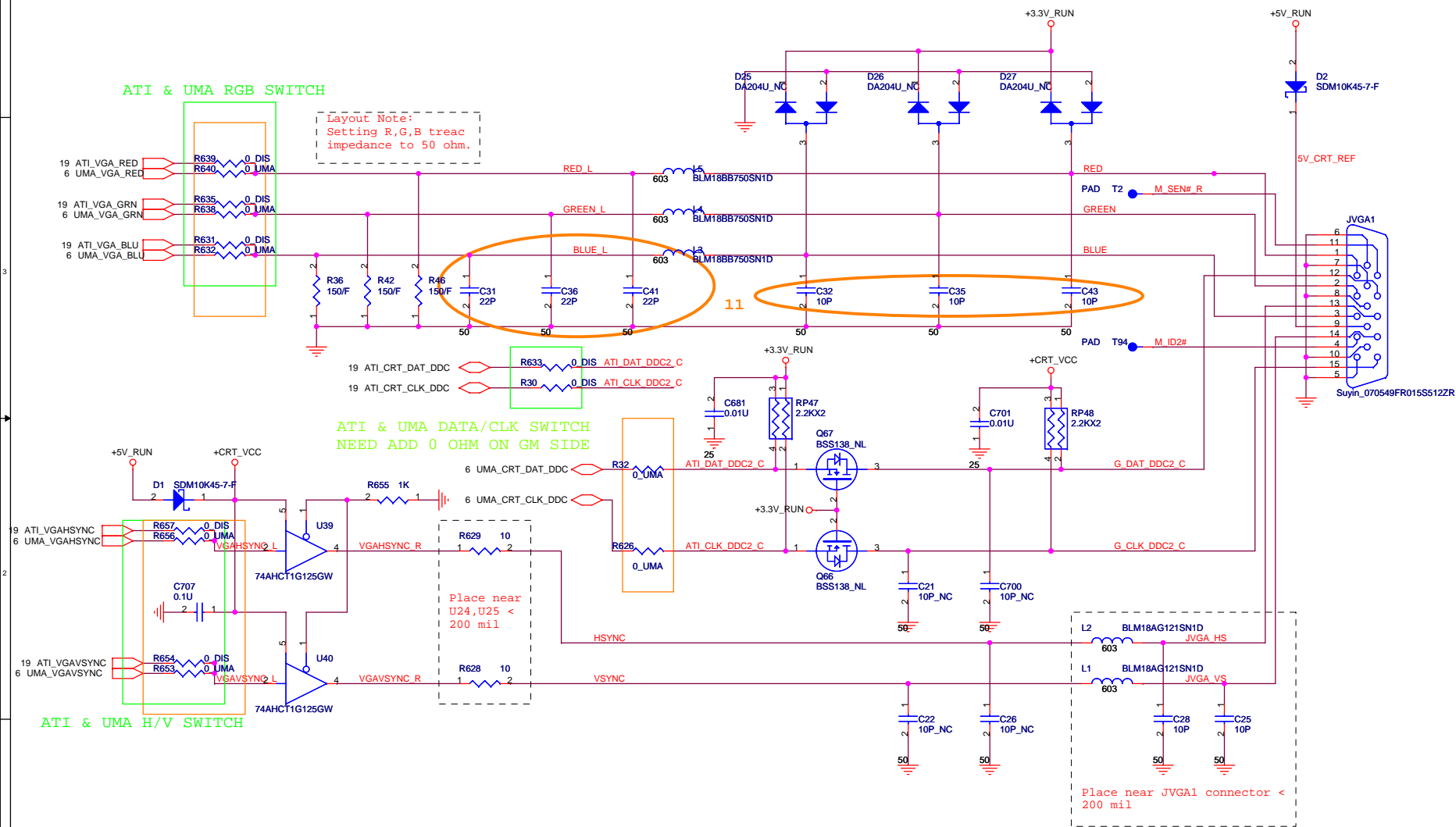
GDDR2 16MX16 MEMORY

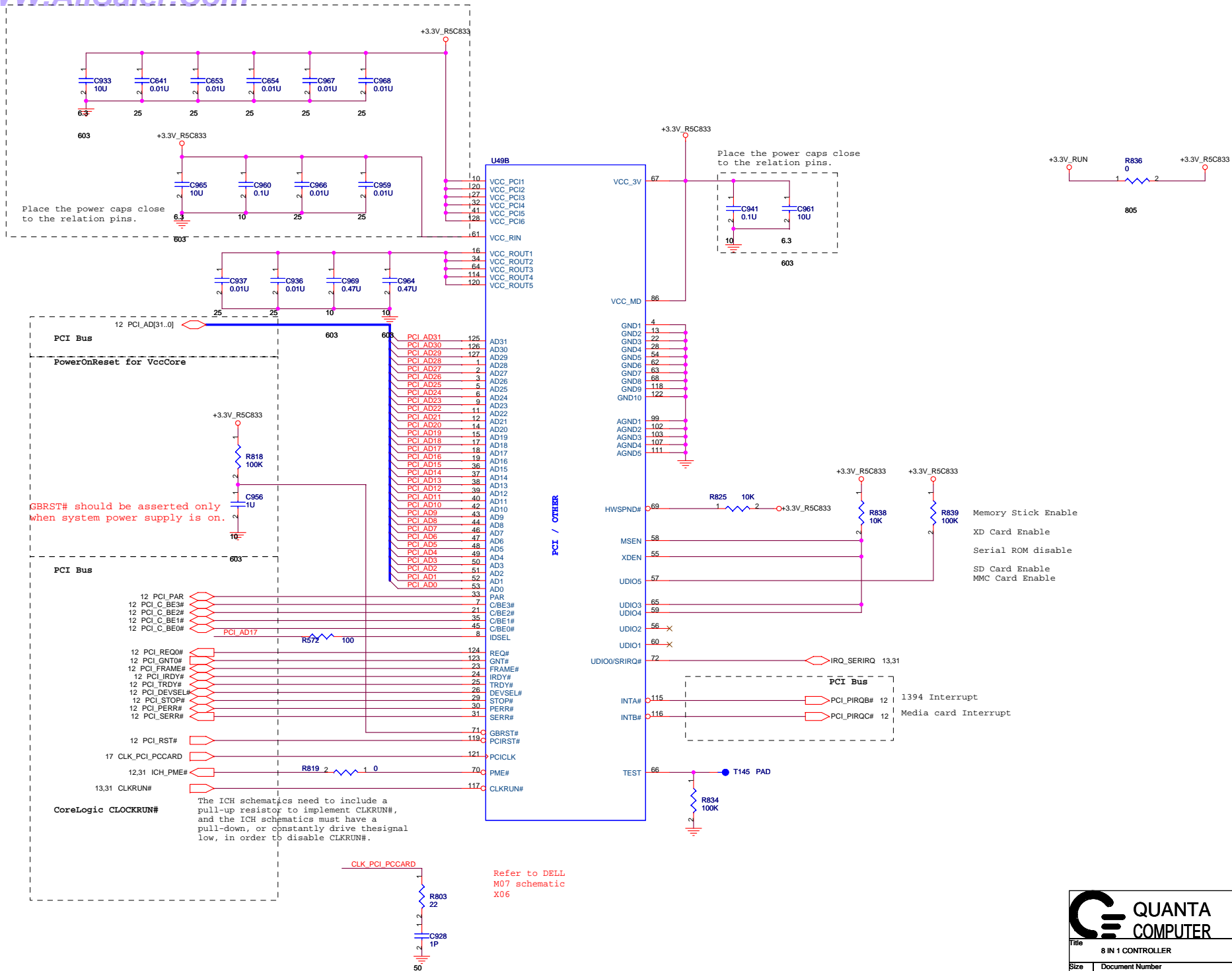
Hynix: AKD5JG-TW09
Samsung: AKD5JG-T507

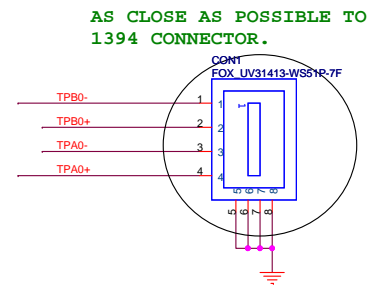
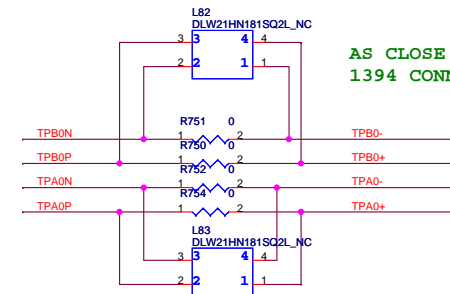
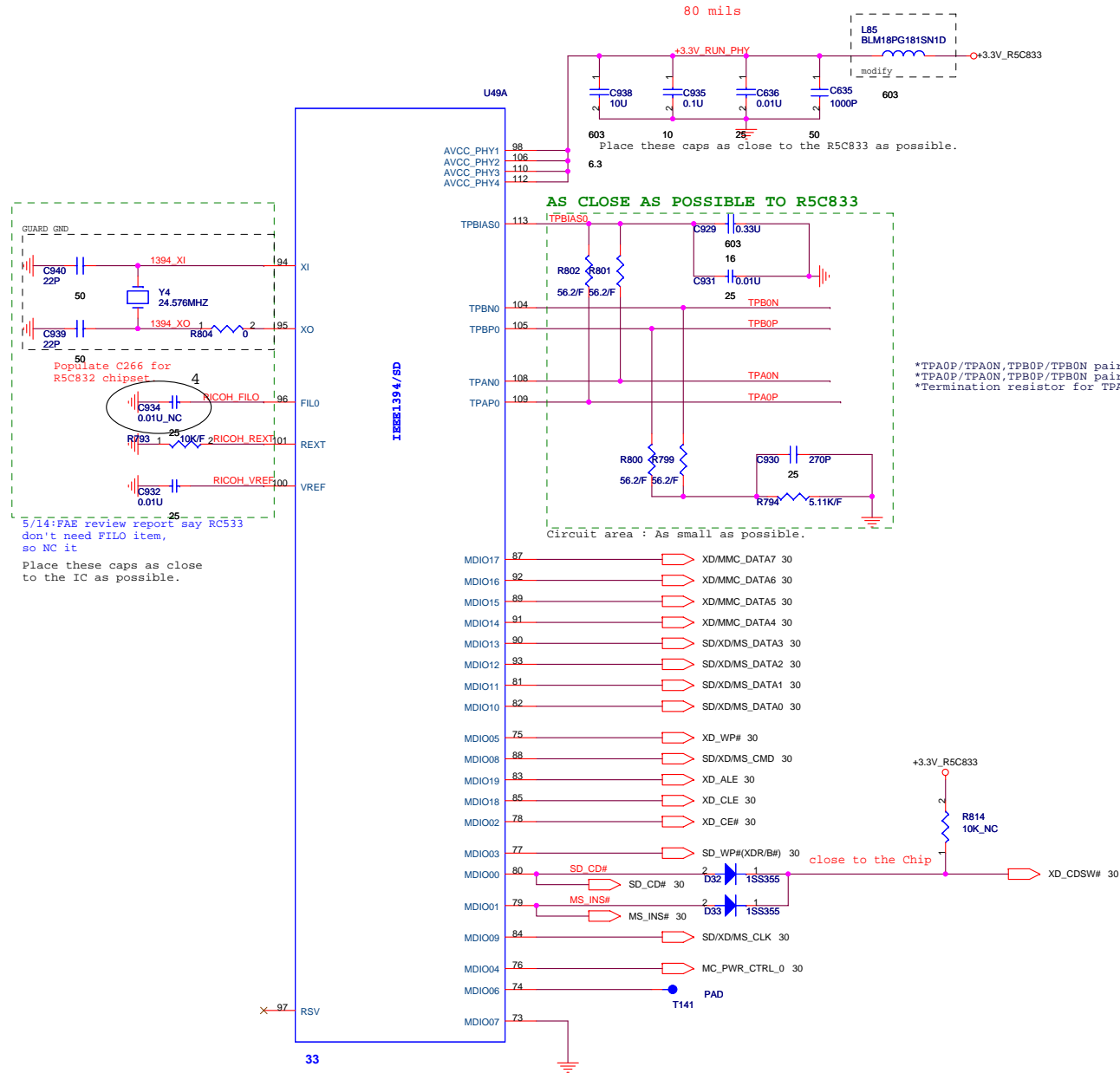


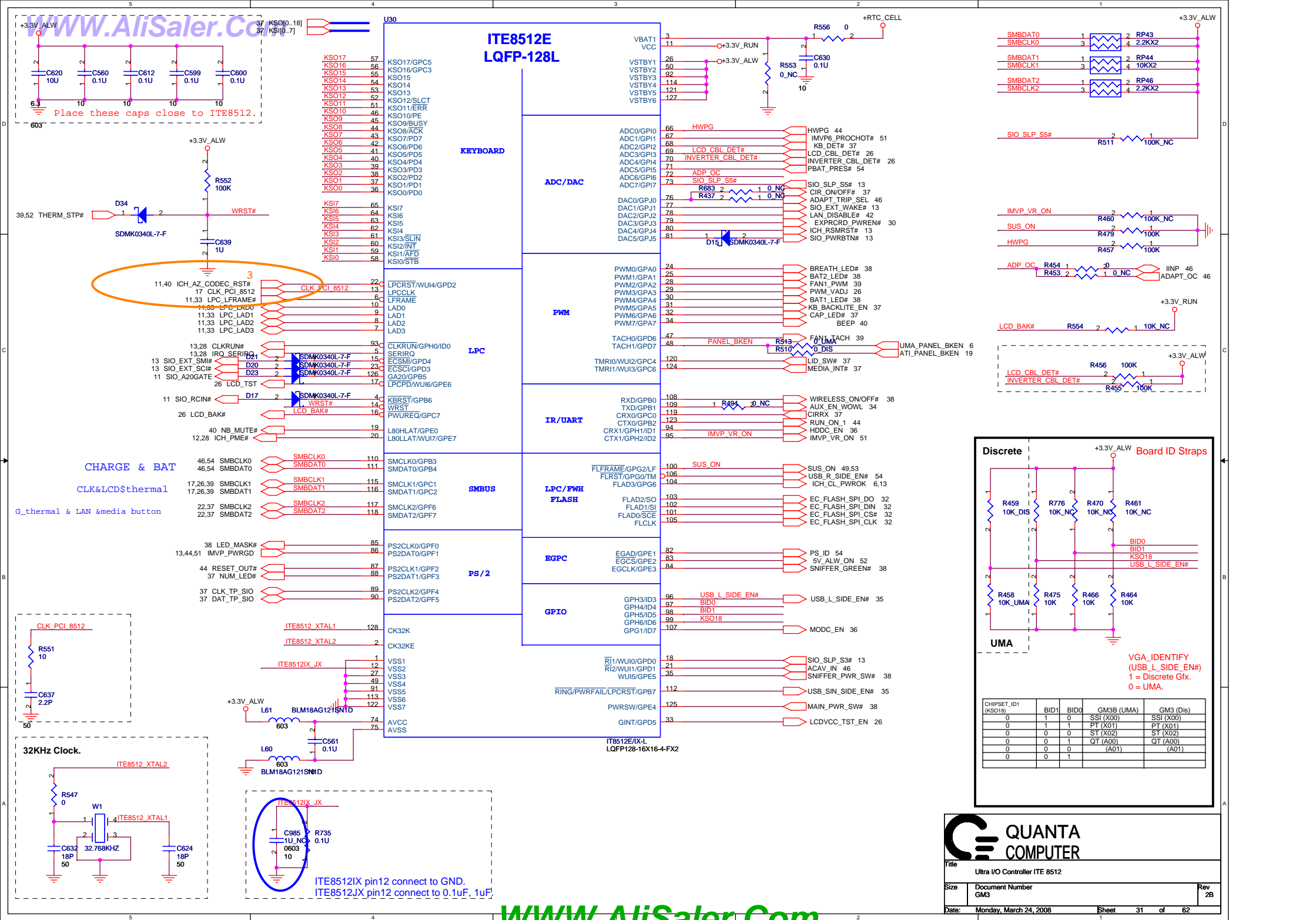




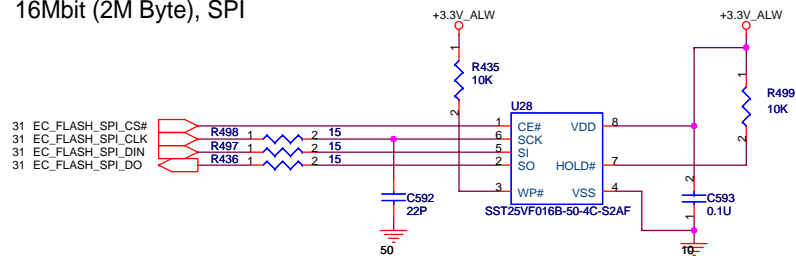




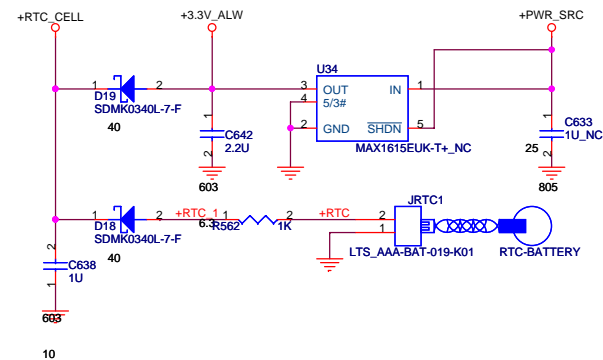




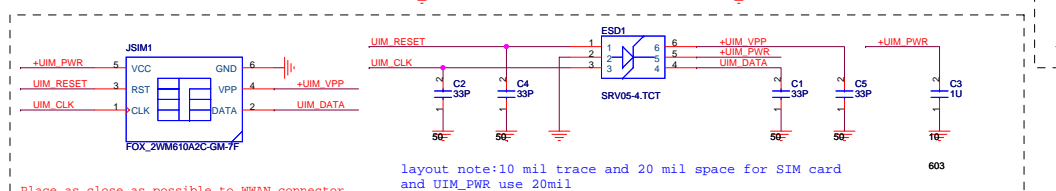
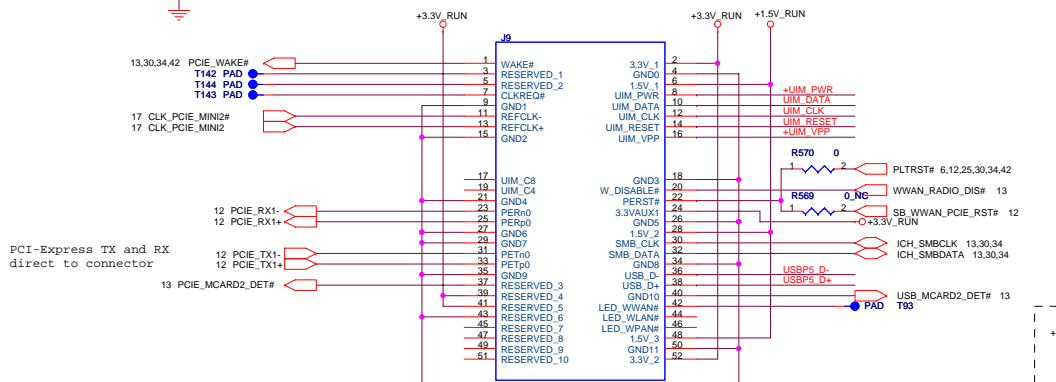
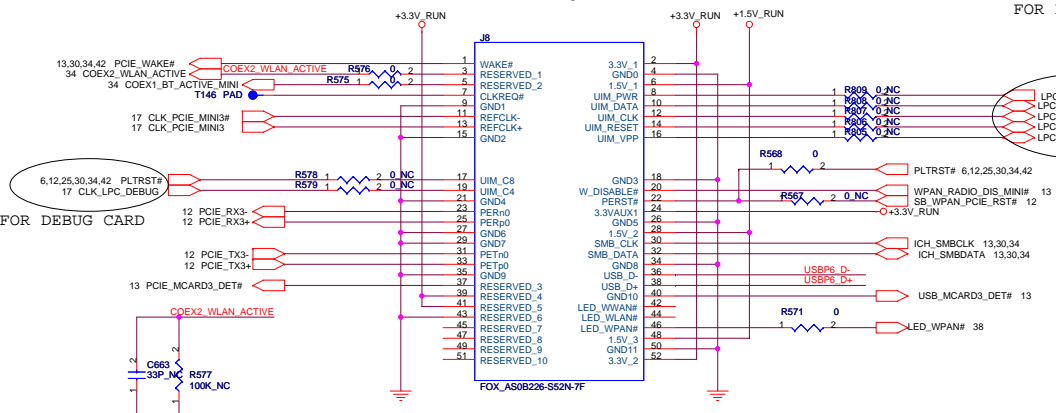
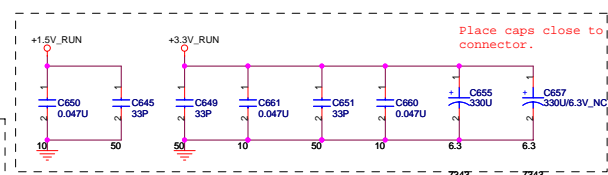
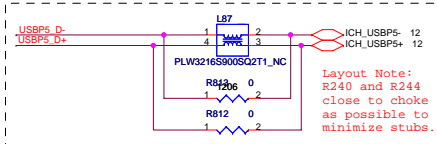
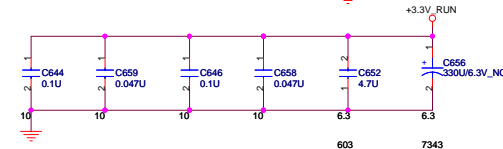
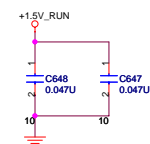
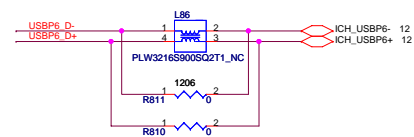
16Mbit (2M Byte), SPI



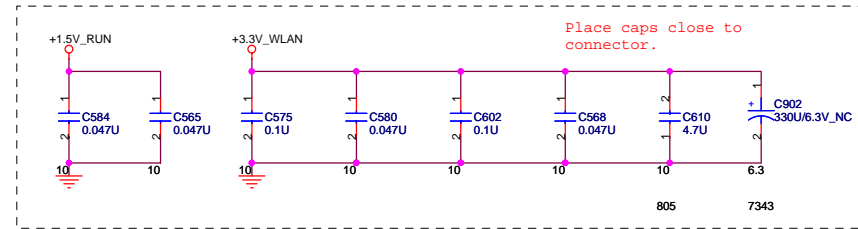
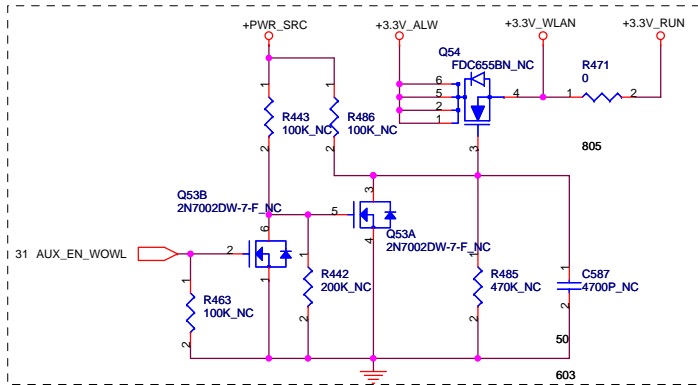
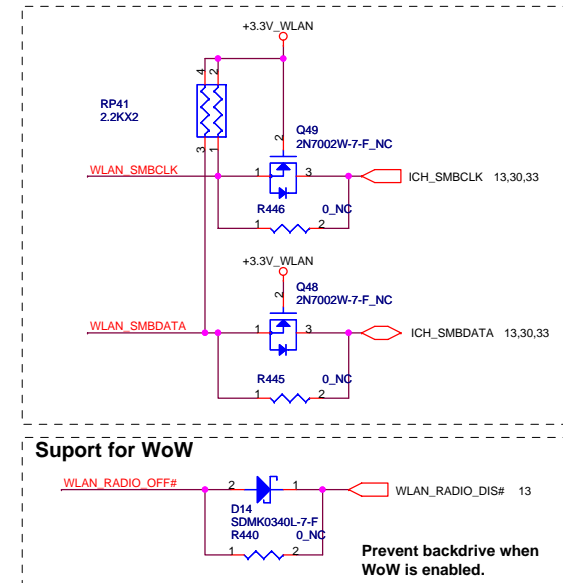
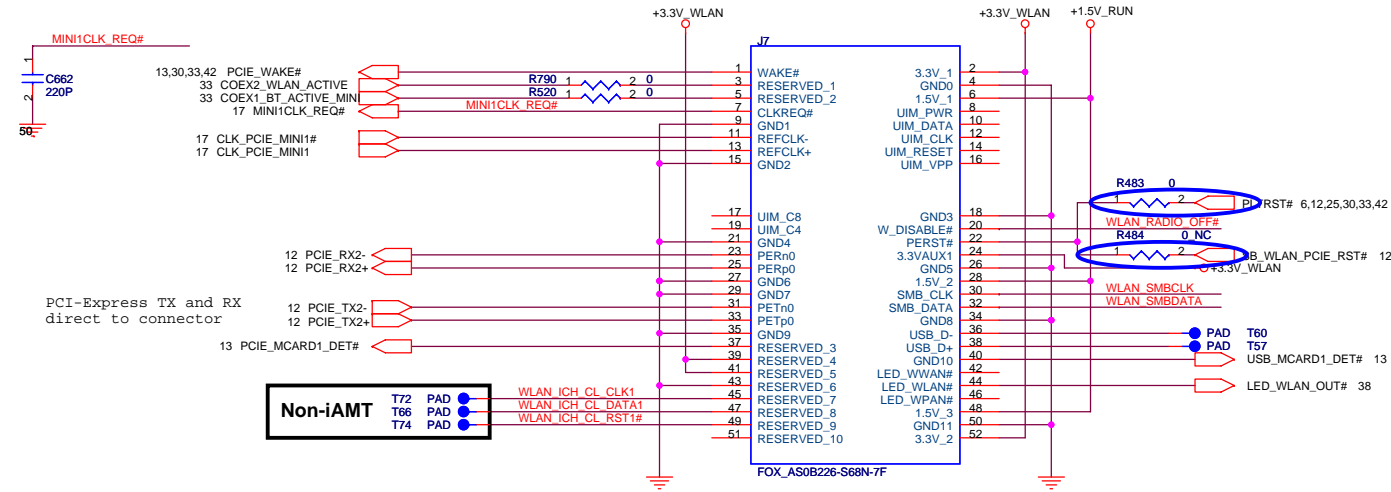
RTC BATTERY

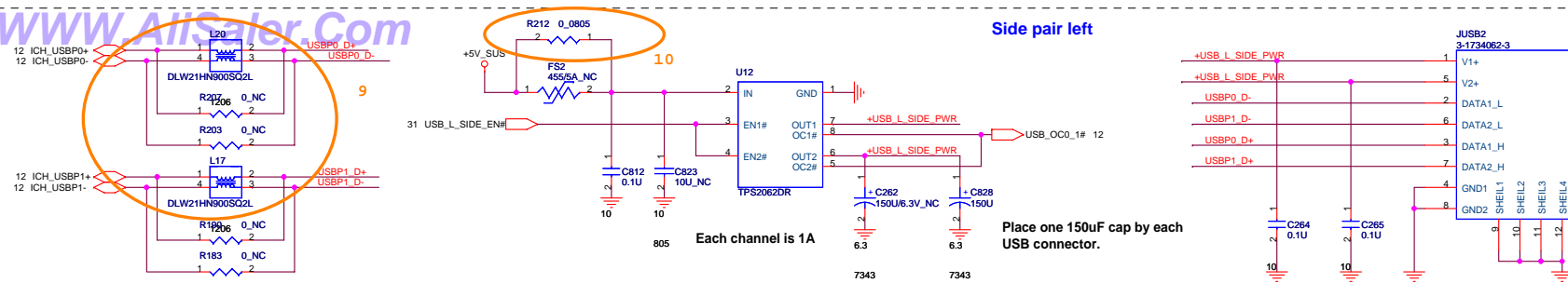


FOR DEBUG CARD

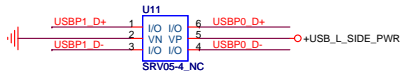


MiniCard WLAN connector

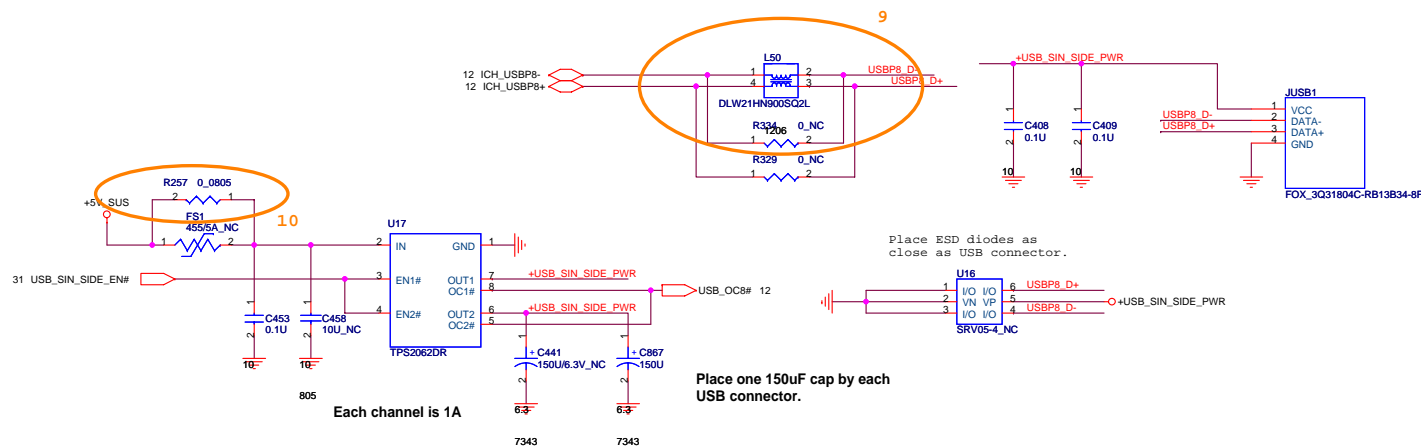




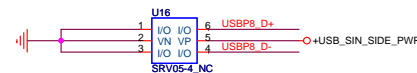
Place ESD diodes as close as USB connector.



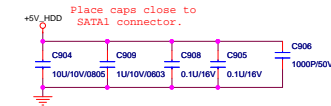
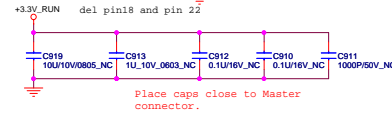
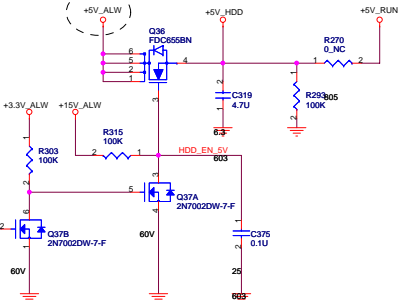
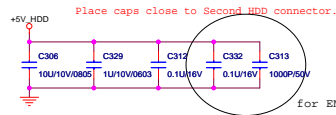
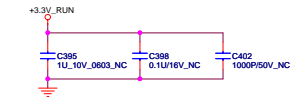
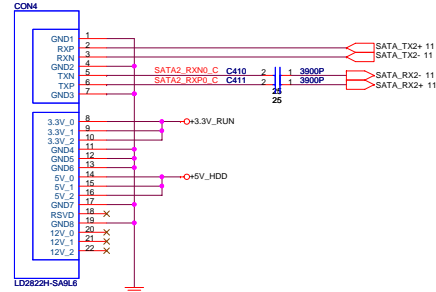
left side single USB port



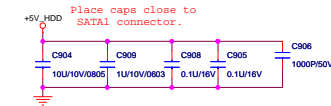
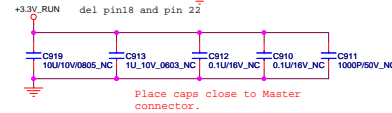
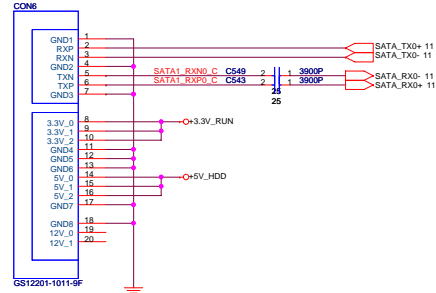
Place ESD diodes as close as USB connector.



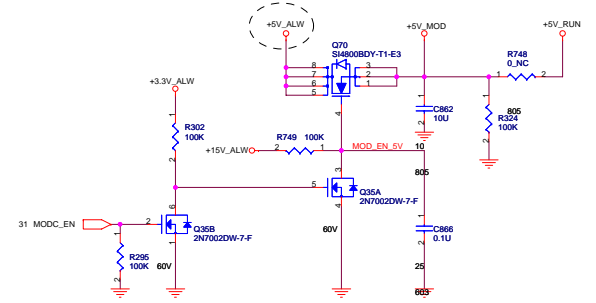
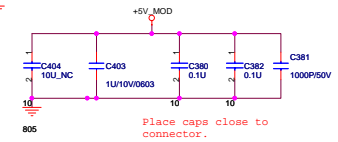
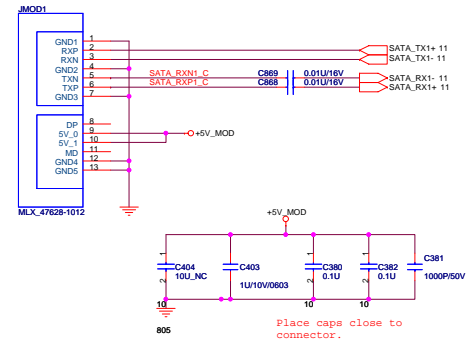
SATA Connector.



Master

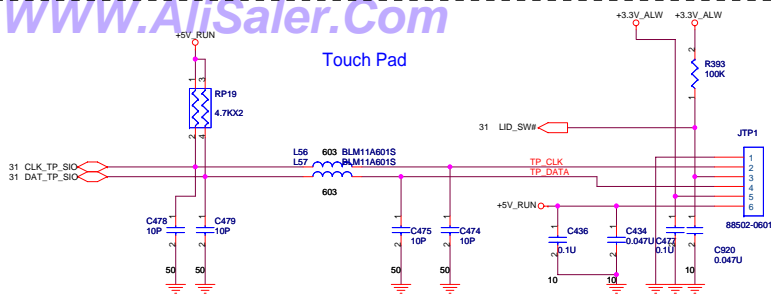


ODD Connector

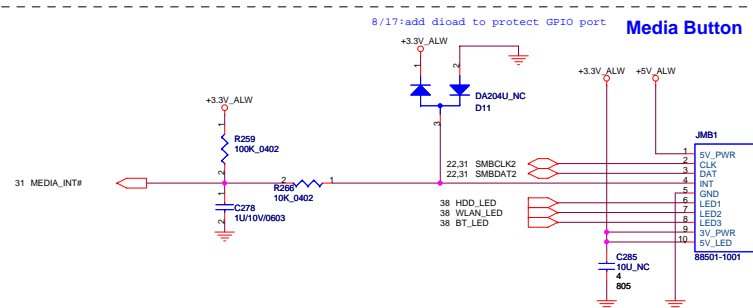


File	SATA (HDD&CD_ROM)
Size	Document Number
GM3	
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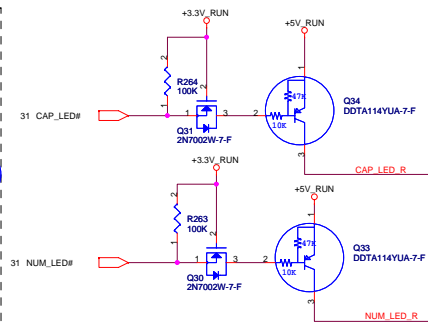
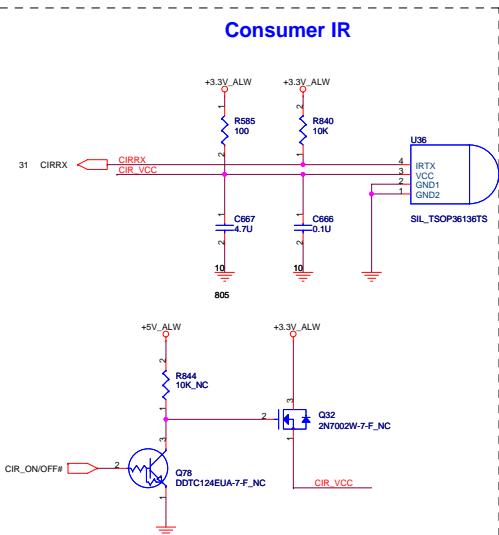
Touch Pad



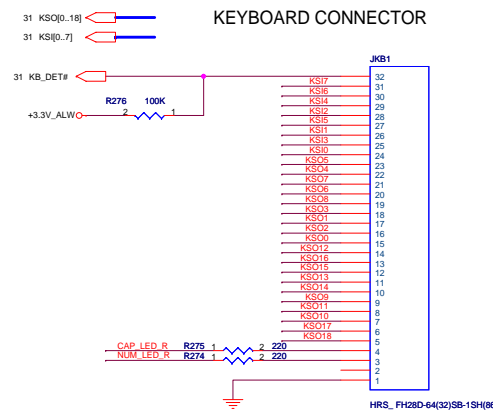
Media Button



Consumer IR



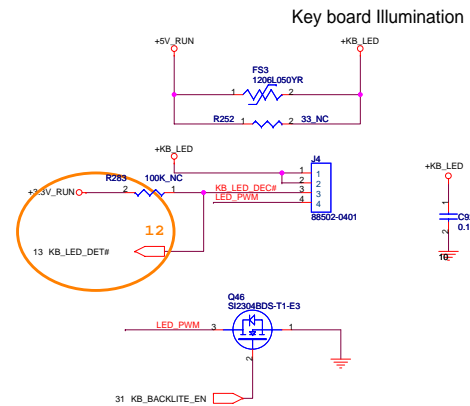
KEYBOARD CONNECTOR



FOR EMI

100P CAPS CLOSE TO JKB1

Key board Illumination



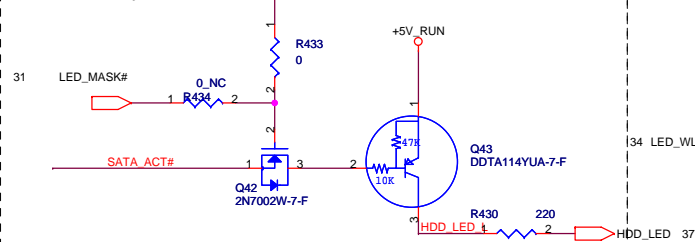
TOUCH PAD, BLUE TOOTH & FIR

Document Number

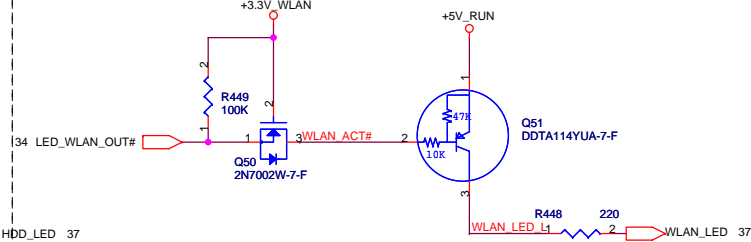
GM3

Rev 2B

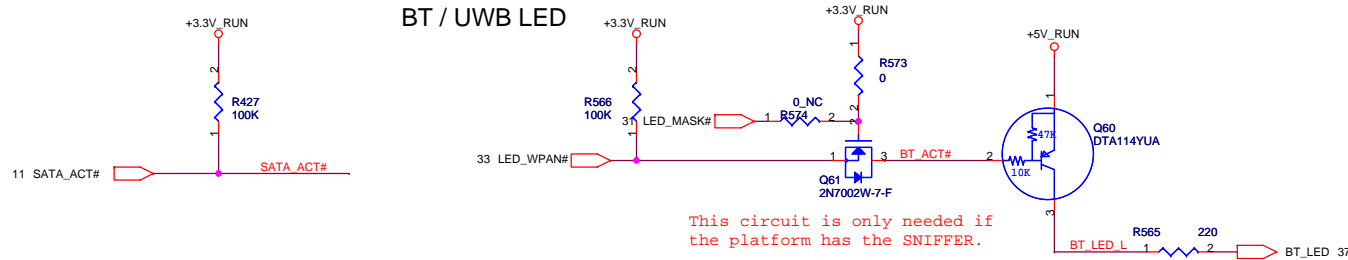
HDD activity LED.



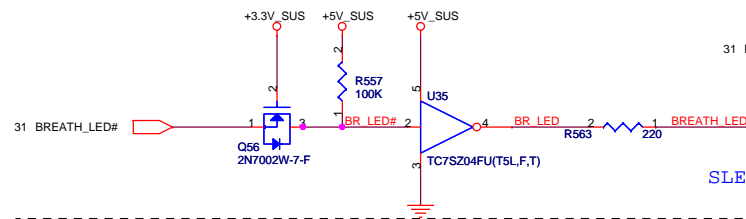
WLAN



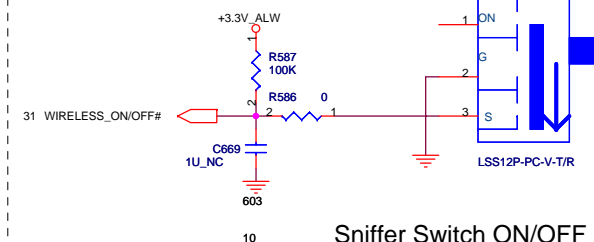
BT / UWB LED



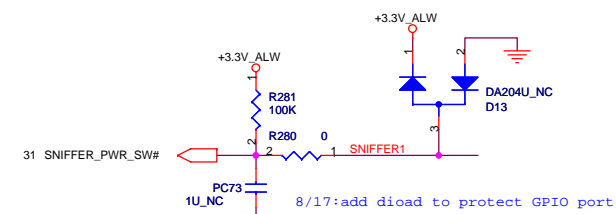
Power & Suspend.



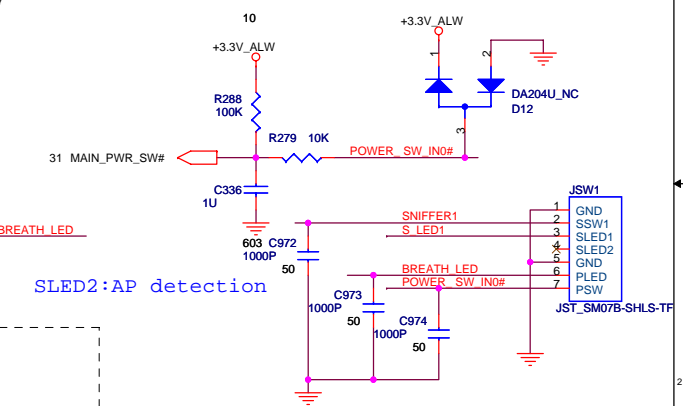
Sniffer Switch



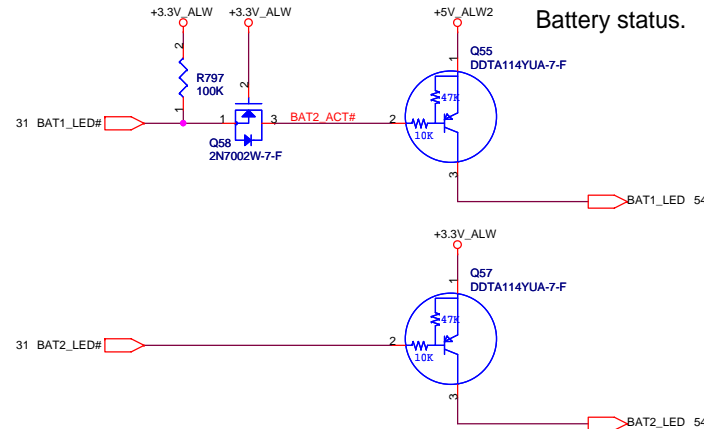
Sniffer Switch ON/OFF



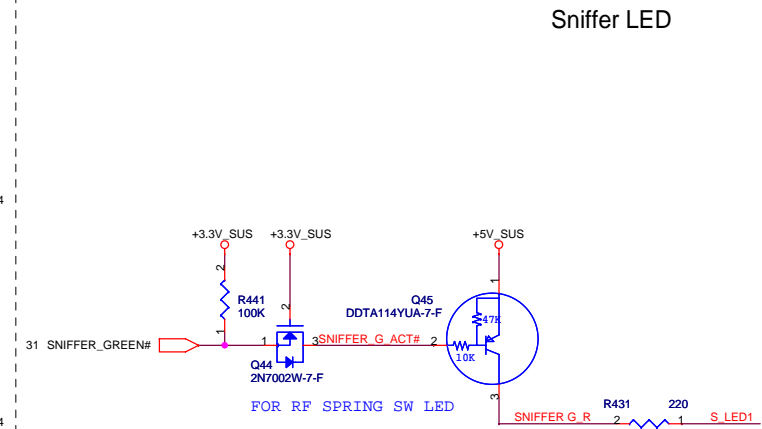
Power Switch



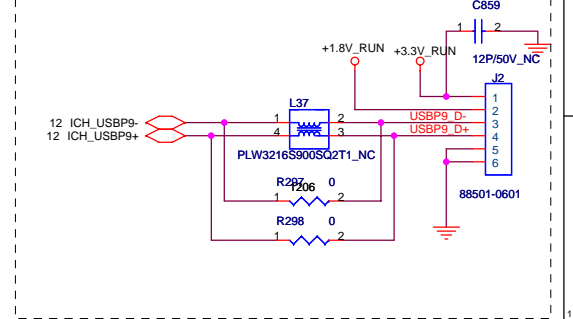
Battery status.



Sniffer LED



Biometric

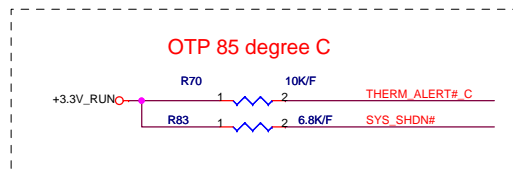
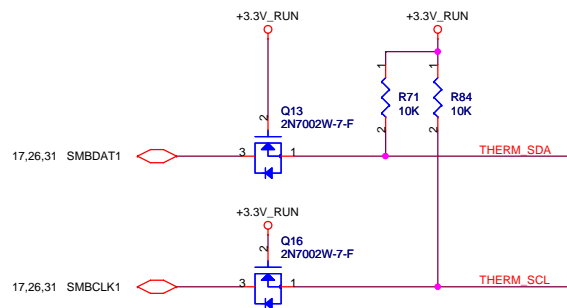
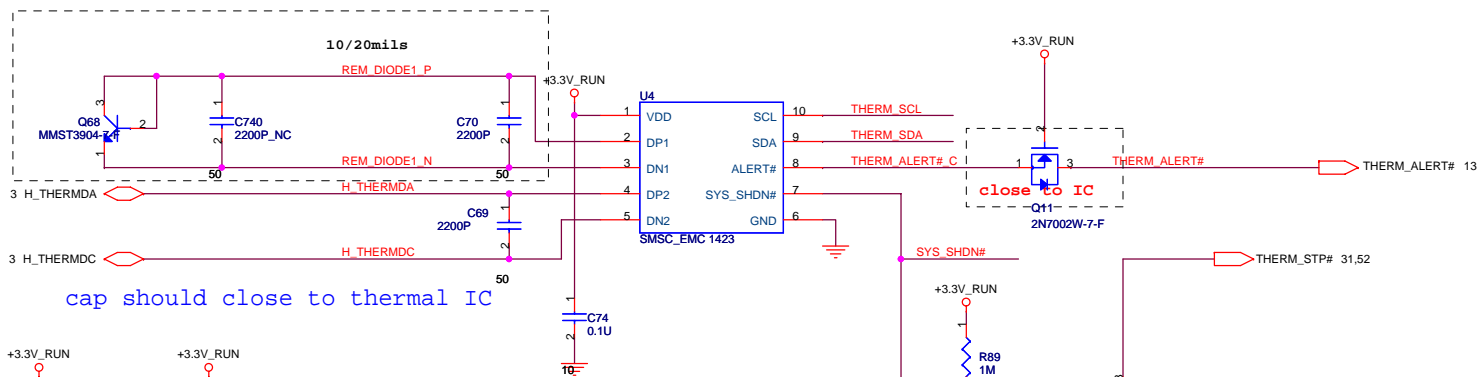
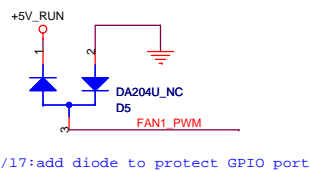
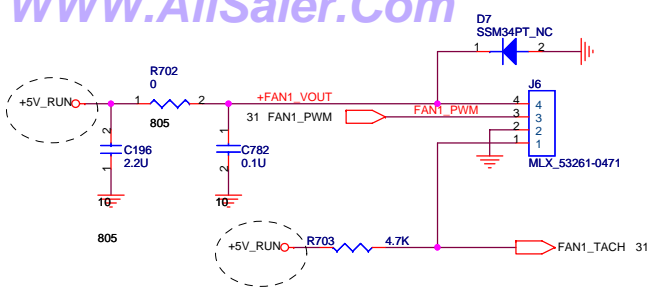


QUANTA COMPUTER

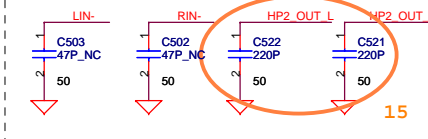
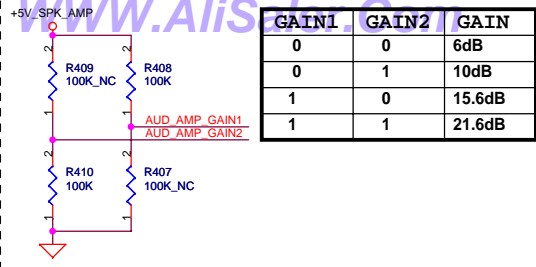
Title: SWITCH, KEYBOARD & LED

Size: Document Number GM3 Rev 2B

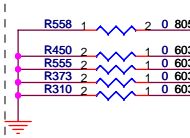
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GAIN1	GAIN2	GAIN
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

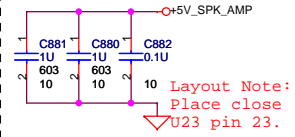
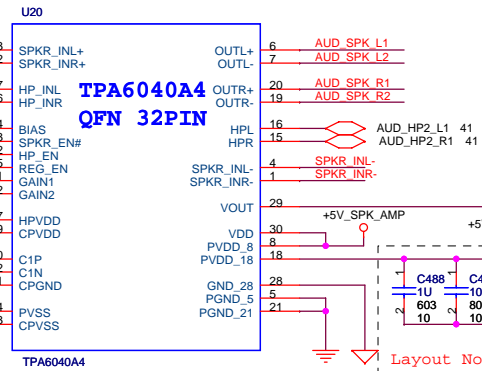


EMI Request

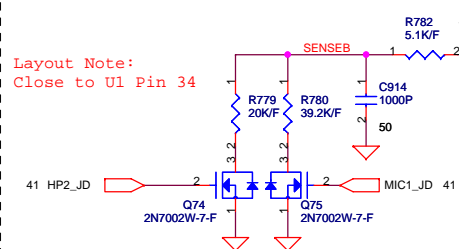


INTERNAL SPEAKER AMP

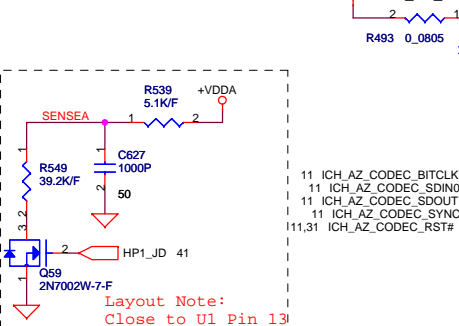
U20
TPA6040A4
QFN 32PIN



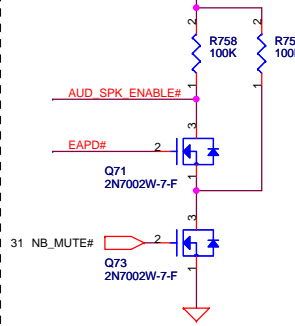
Layout Note:
Close to U1 Pin 34



Layout Note:
Close to U1 Pin 34



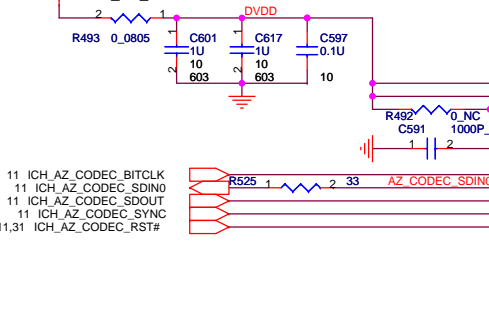
Layout Note:
Close to U1 Pin 13



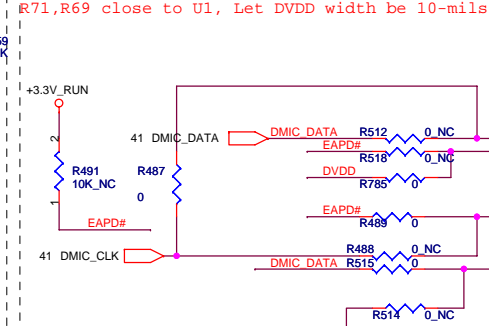
Layout Note:
Close to U1 Pin 13

Layout Note:
Close to U1 Pin 13

FB_60ohm+-25%_100MHz
_3A_0.05ohm DC



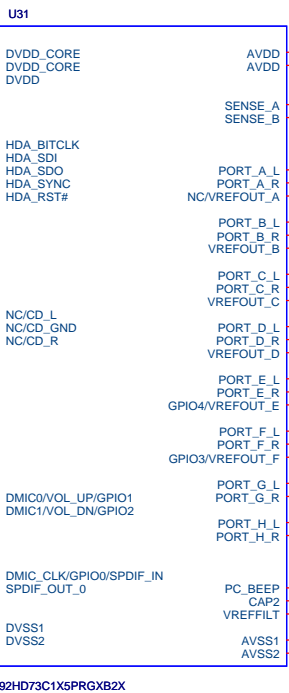
Layout Note:
Close to U1 Pin 13



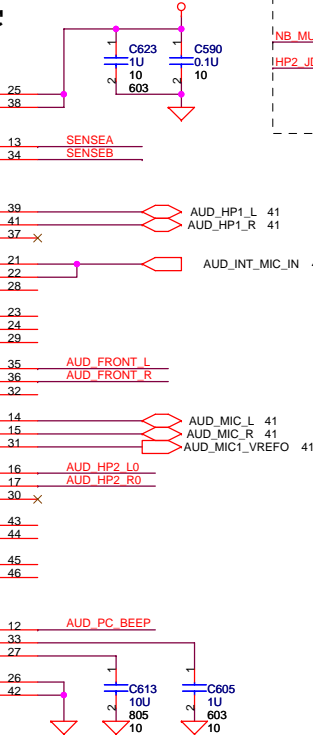
Layout Note:
Close to U1 Pin 13

Layout Note:
Close to U1 Pin 13

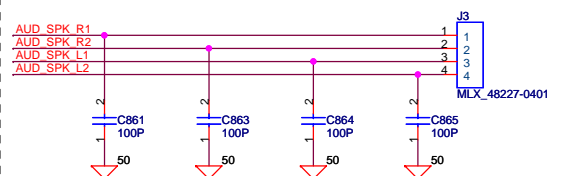
AZALIA (HD) CODEC



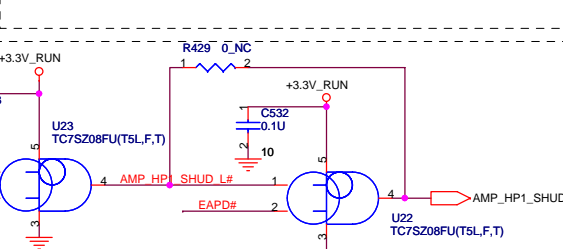
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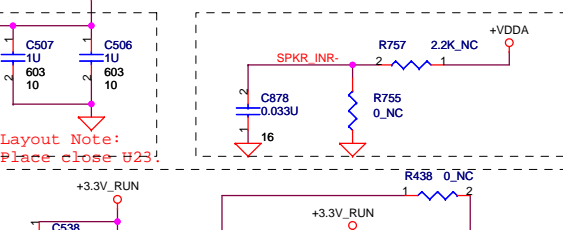
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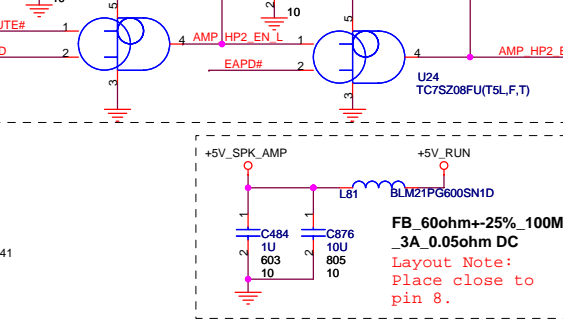
Layout Note:
Close to U1 Pin 13



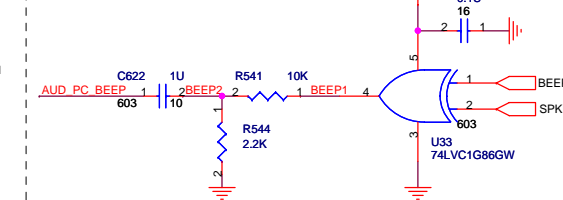
Layout Note:
Close to U1 Pin 13



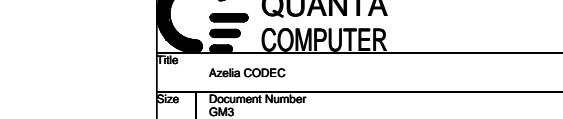
Layout Note:
Close to U1 Pin 13



Layout Note:
Close to U1 Pin 13



Layout Note:
Close to U1 Pin 13



Layout Note:
Close to U1 Pin 13

Layout Note:
Close to U1 Pin 13



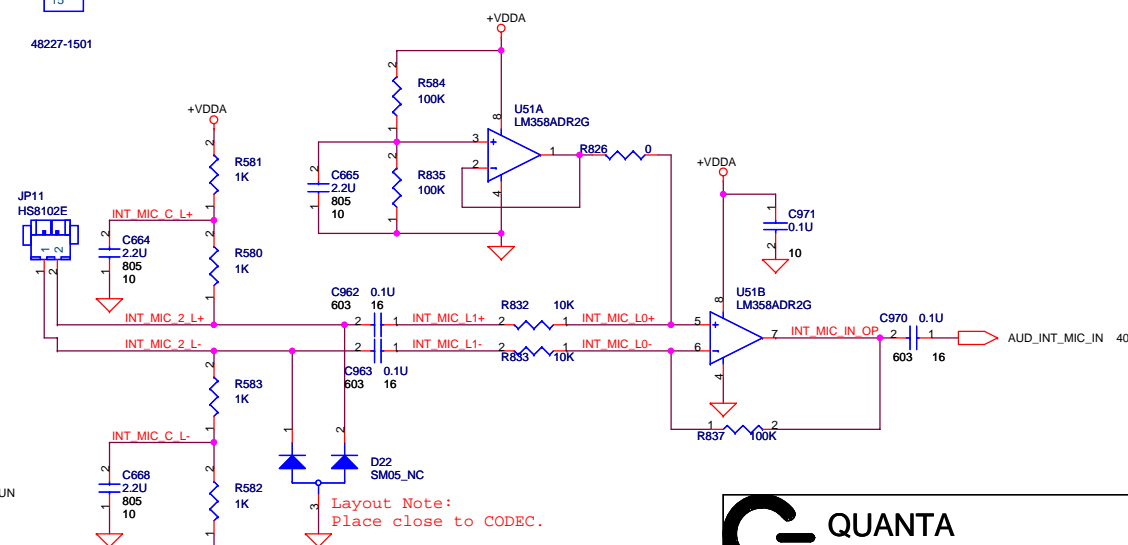
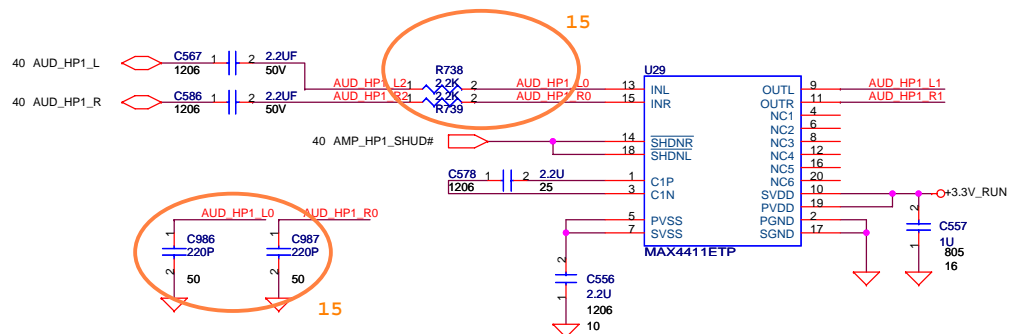
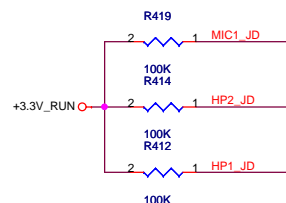
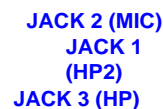
Title
Azalia CODEC


Size
GM3

Date: Monday, March 24, 2008

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Rev 2B



 QUANTA COMPUTER	
Title: AUDIO CONN	
Size:	Document Number GM3
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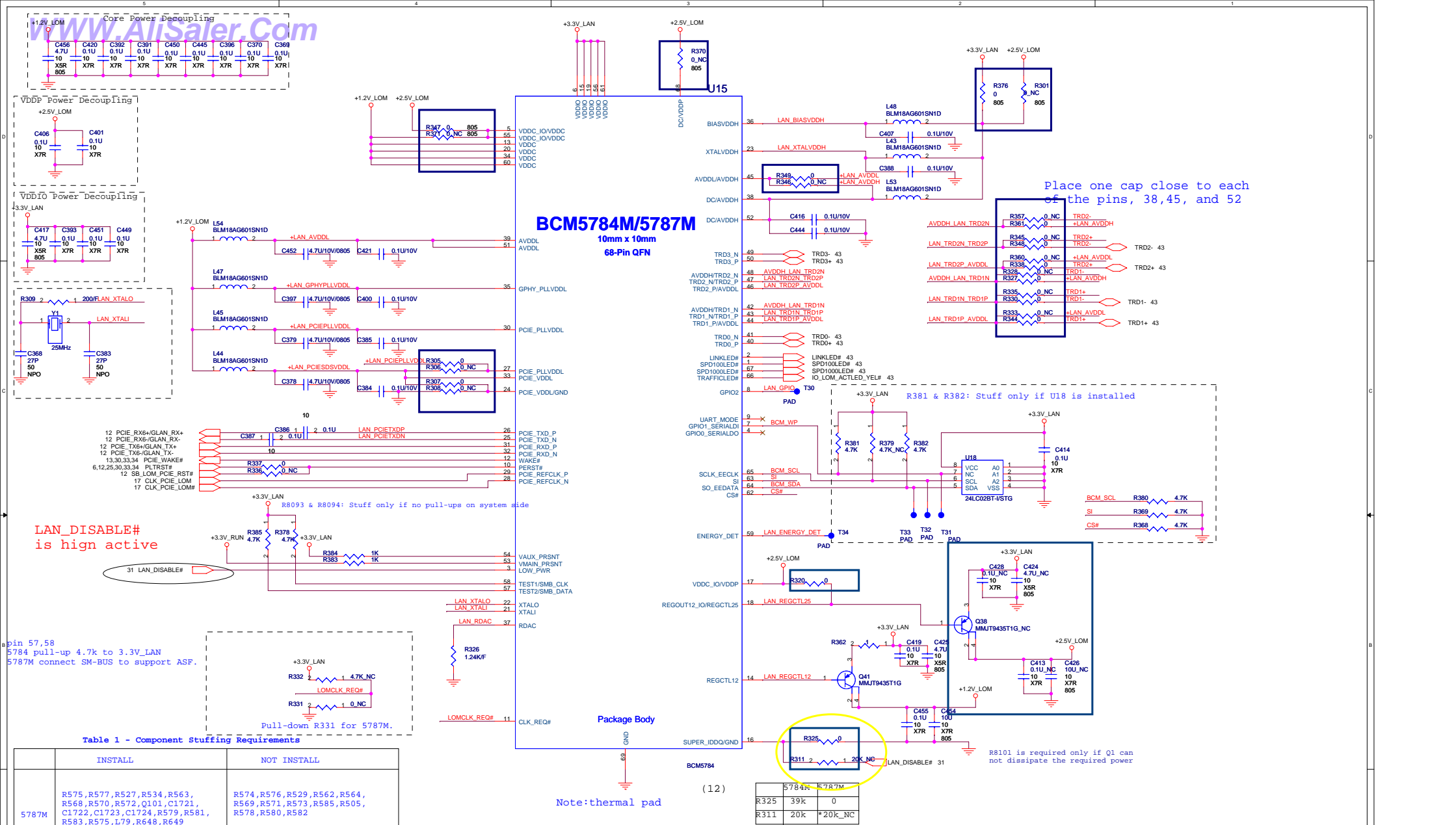
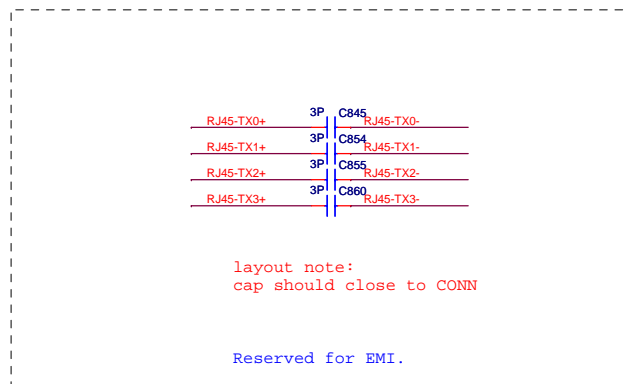
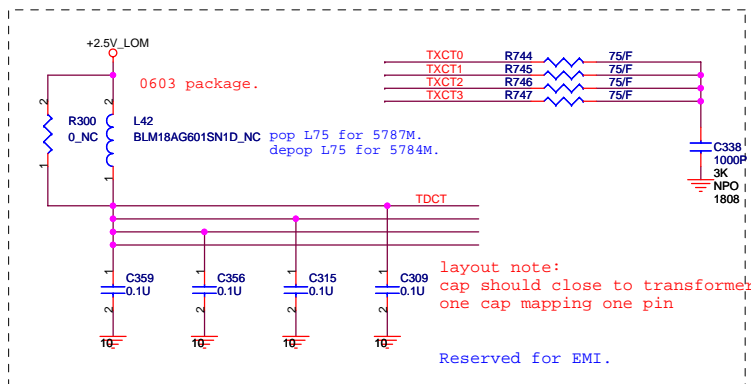
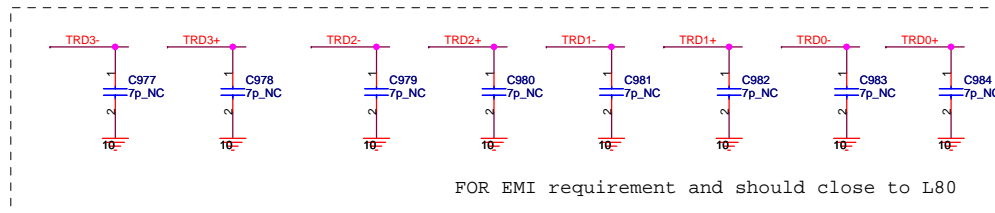
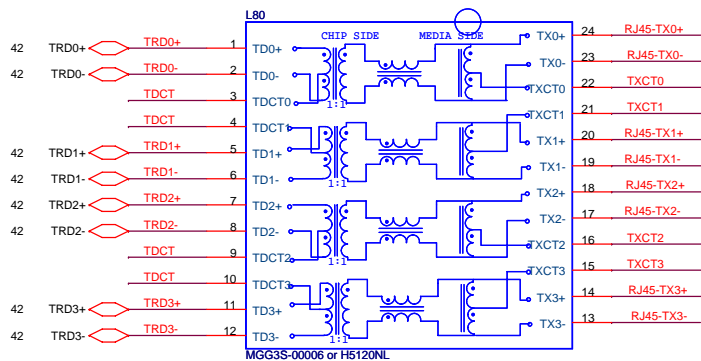


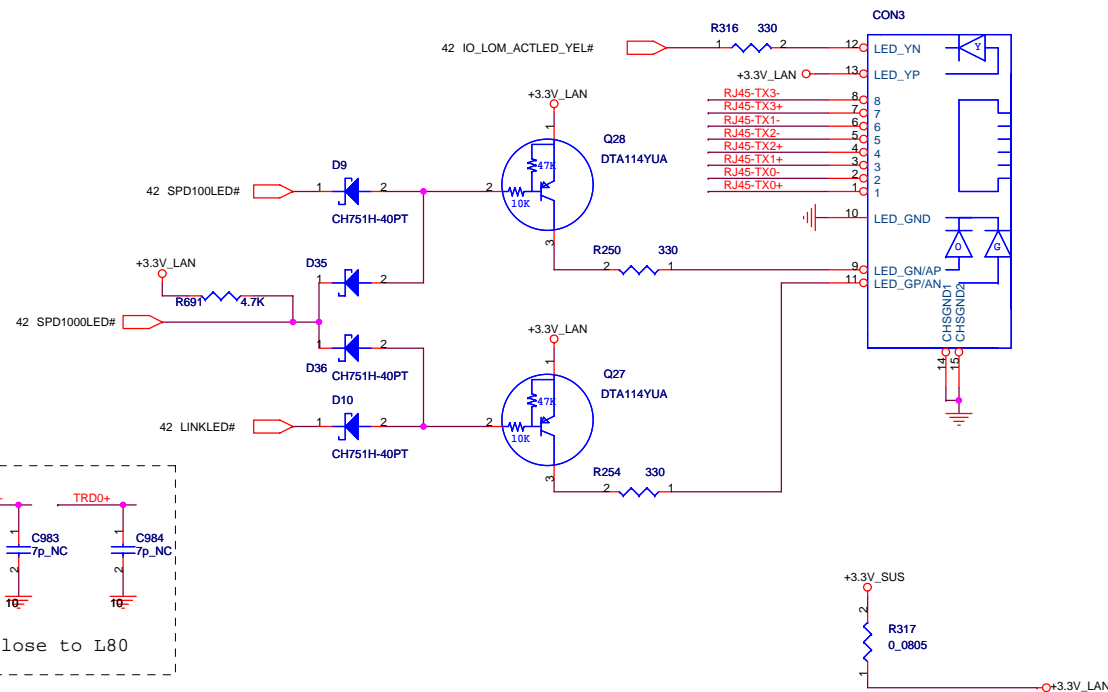
Table 1: Equipment Selection Requirements		
	INSTALL	NOT INSTALL
5787M	R575, R577, R527, R534, R563, R568, R570, R572, Q101, C1721, C1722, C1723, C1724, R579, R581, R583, R575, L79, R648, R649	R574, R576, R529, R562, R564, R569, R571, R573, R585, R505, R578, R580, R582
5784	R574, R576, R529, R562, R564, R569, R571, R573, R585, R505, R578, R580, R582	R575, R577, R527, R534, R563, R568, R570, R572, Q101, C1721, C1722, C1723, C1724, R579, R581, R583, R575, L79, R648, R649

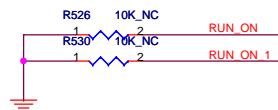
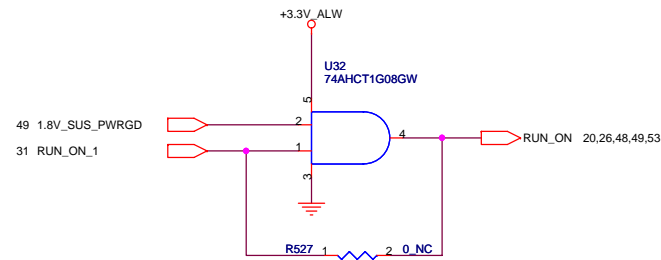
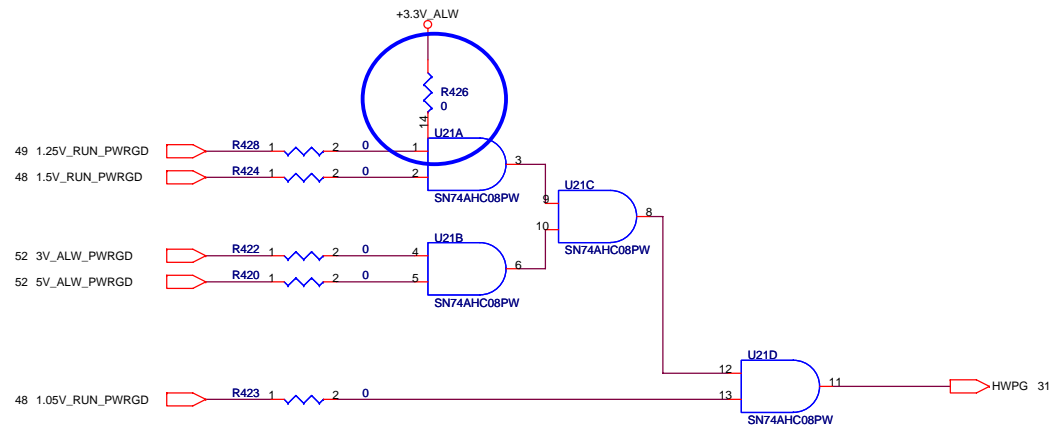
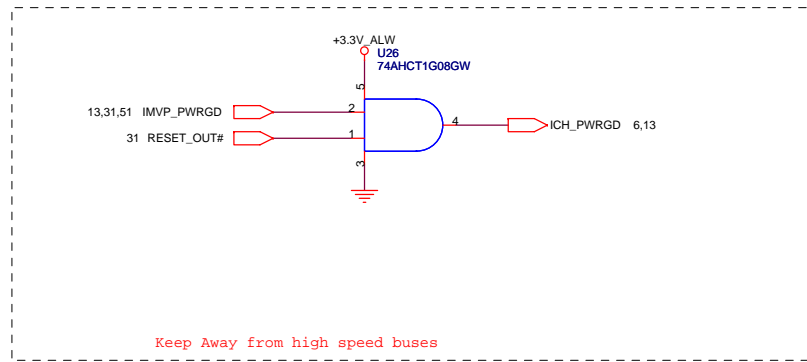
WWW.AliSaler.Com

TRANSFORM




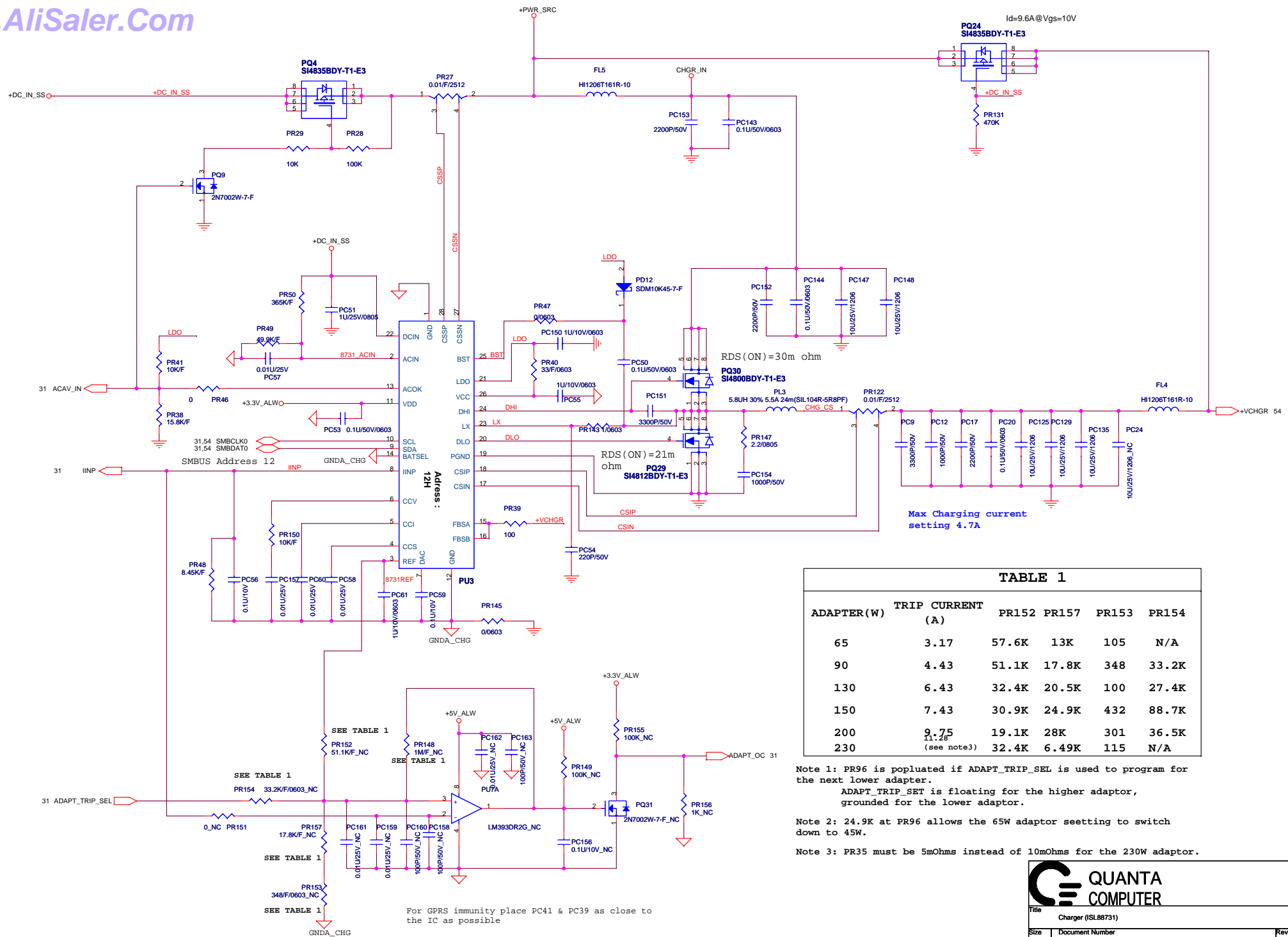
RJ-45 Connector








 QUANTA COMPUTER			
Title Battery Selector			
Size	Document Number GM3		
			Rev 2B
Date:	Monday, March 24, 2008	Sheet	45 of 62

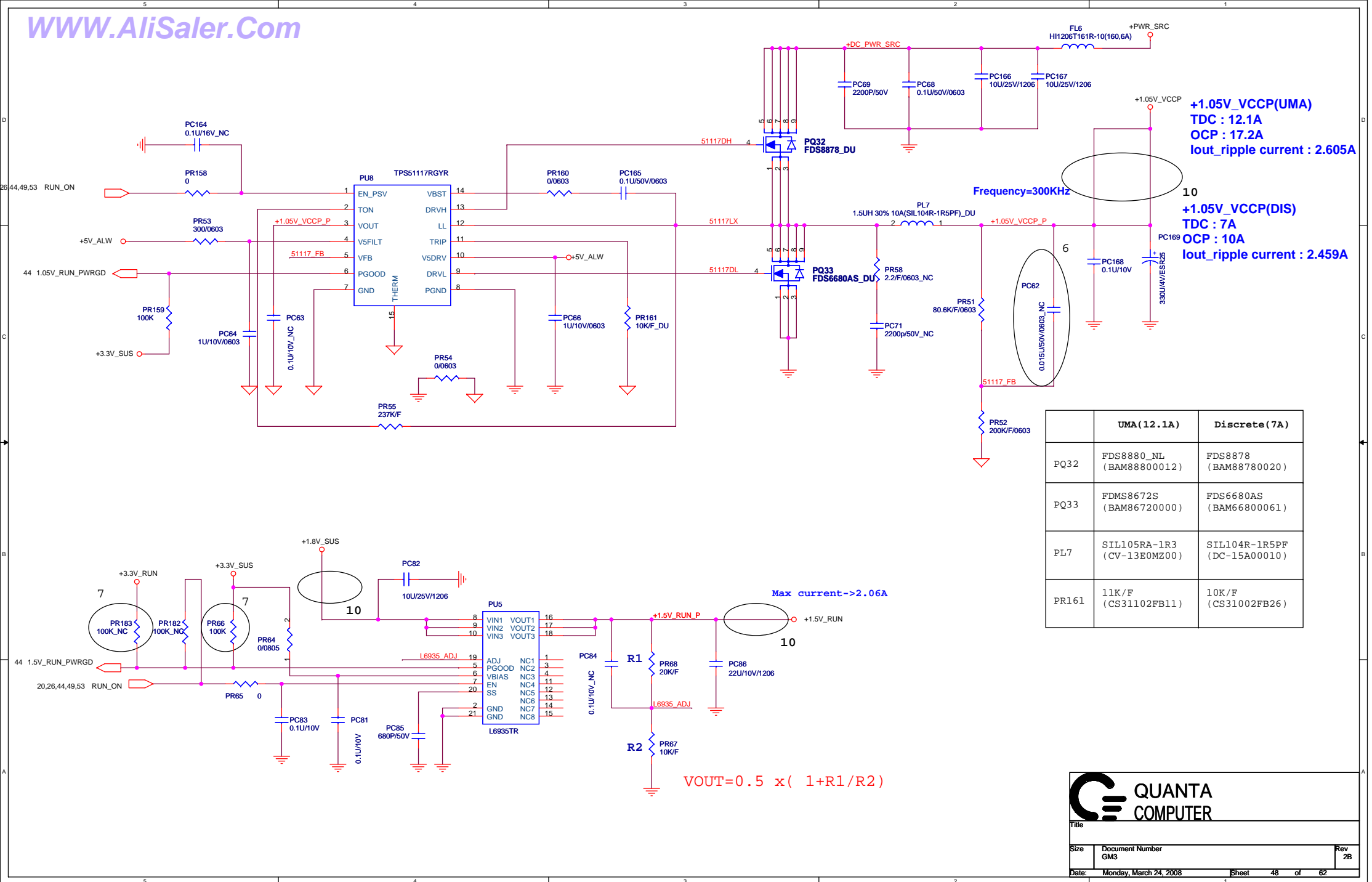


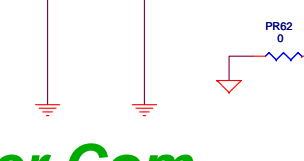
Charger (ISL88731)

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	GM3	2B
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NUMBER SAME AS DISCRETE**


 QUANTA COMPUTER		
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+1.8V_SUS(UMA)
TDC : 10.25A
OCP : 14.9A
Iout_ripple current : 4.868A




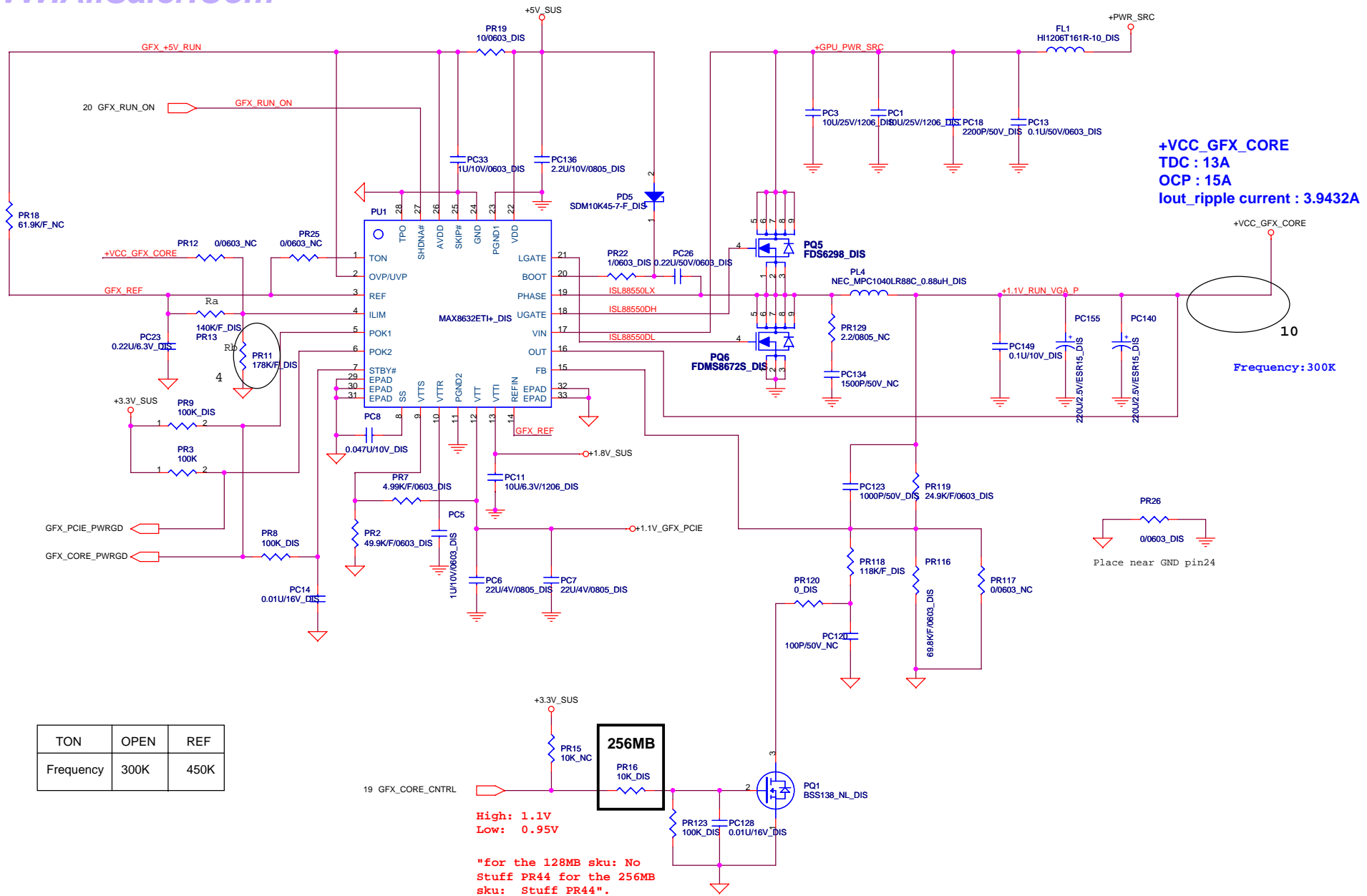




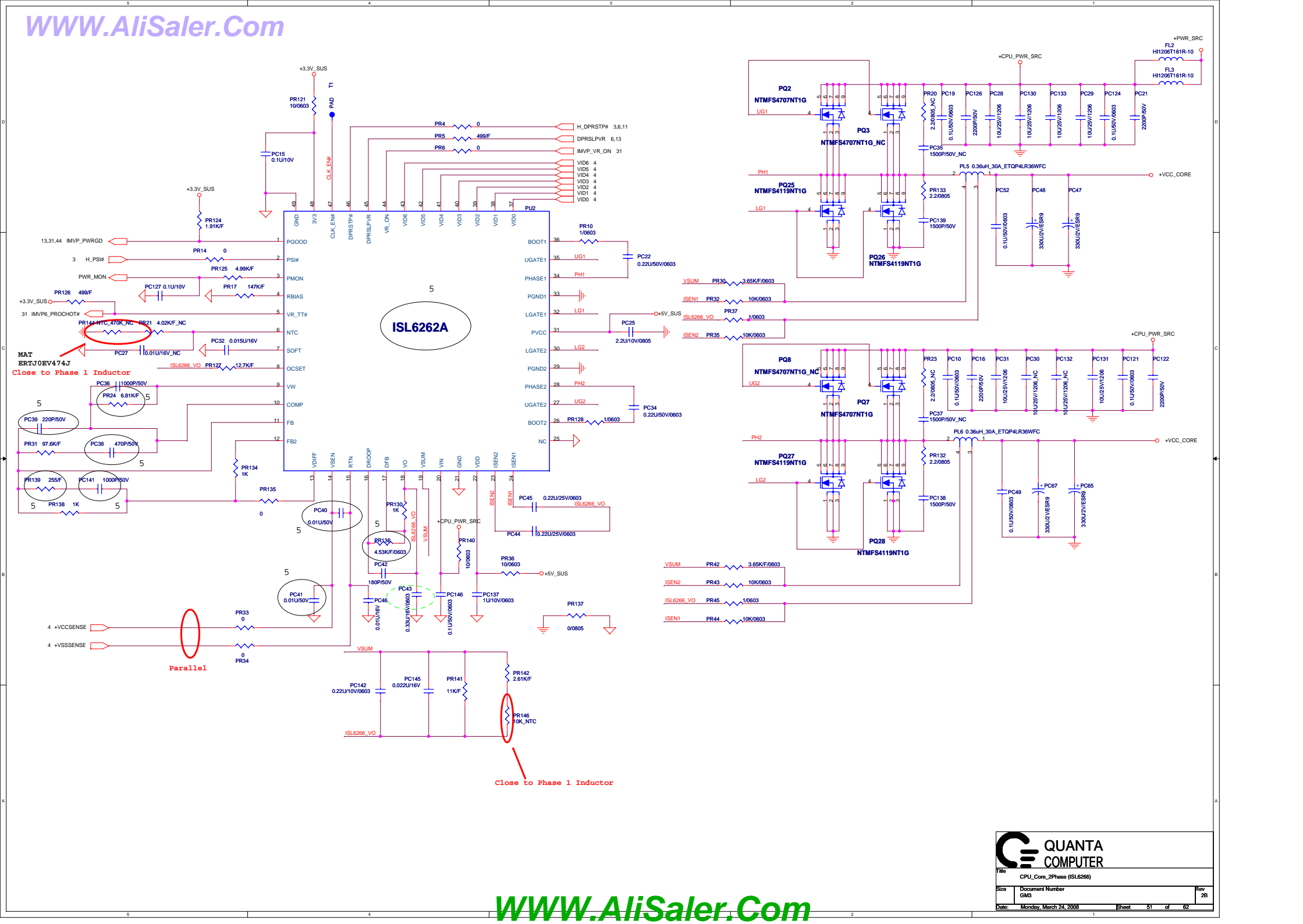
**QUANTA
COMPUTER**

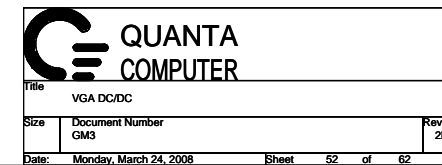
Title	
1.8VSUS & 0.9VTT (TPS51116)	
Size	Document Number
	GM3
Date:	Monday, March 24, 2008

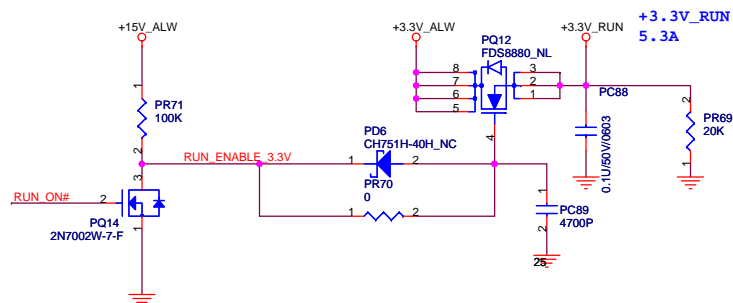
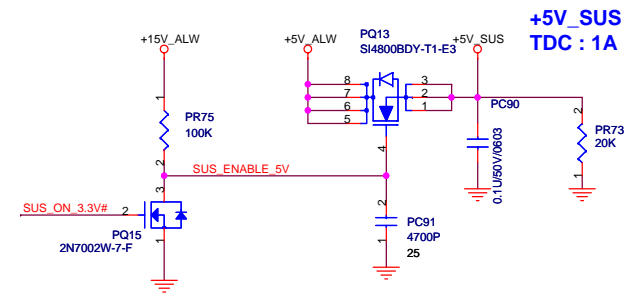
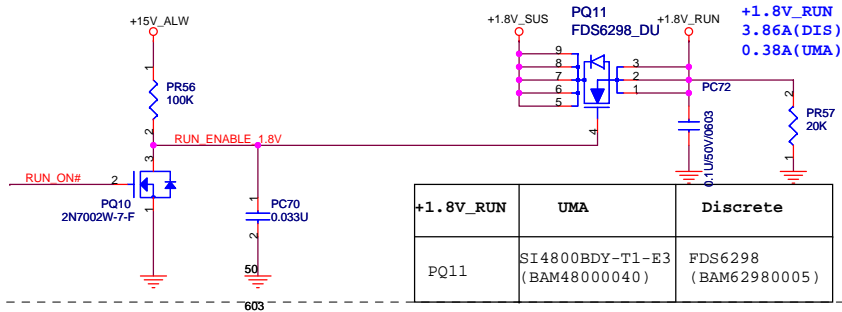
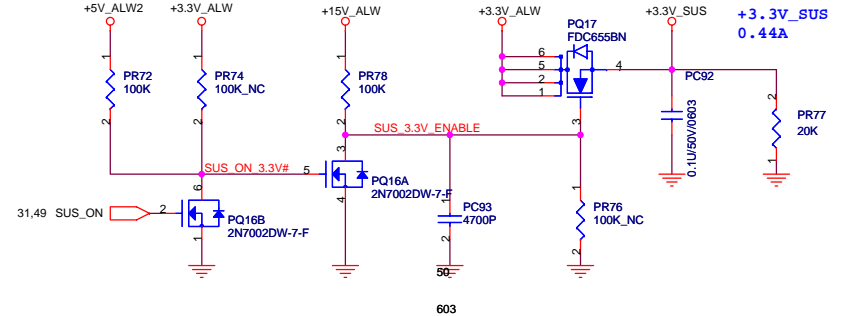
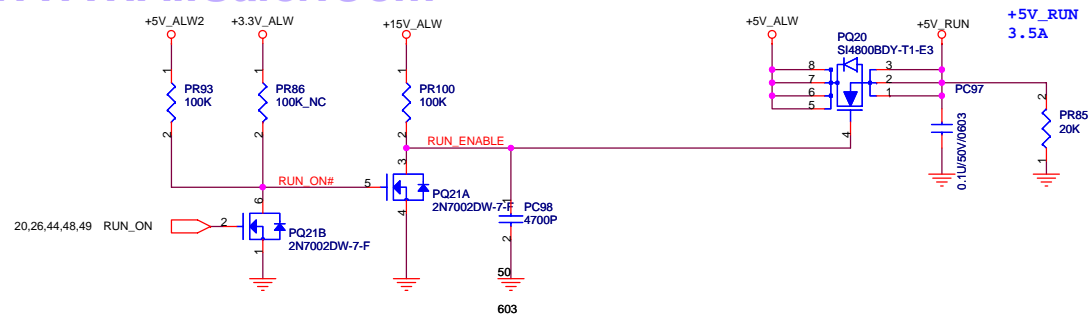
 <div> <h1>QUANTA</h1> <h1>COMPUTER</h1> </div>		
<div>Title</div> <div>1.6VSUS & 0.9VTT (TPS51116)</div>		
<div>Size</div> <div> <div>Document Number</div> <div>GM3</div> </div>	<div>Rev</div> <div>2B</div>	
<div>Date:</div> <div>Monday, March 24, 2008</div>	<div>Sheet</div> <div>40</div>	<div>of</div> <div>62</div>



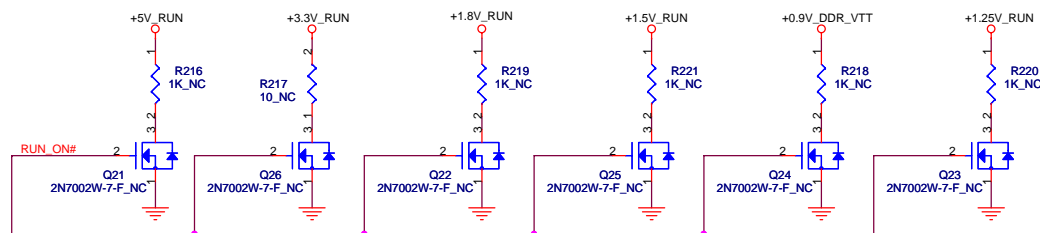
ILIM	$I_{ovp} = (2 * (R_b / (R_a + R_b)) * 0.1 * (1 / R_{DS(on)}) + (I_{\Delta} / 2)$
SKIP#	AVDD = Low-noise, forced-PWM mode. GND = Pulse-skipping operation.
OVP/UVF	The overvoltage limit is 116% of Vout. The undervoltage limit is 70% of Vout.



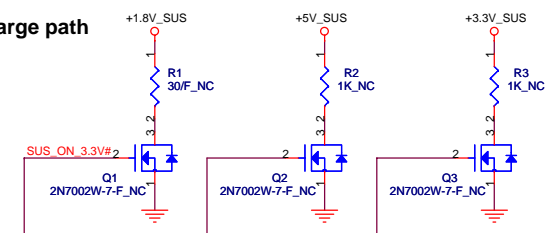




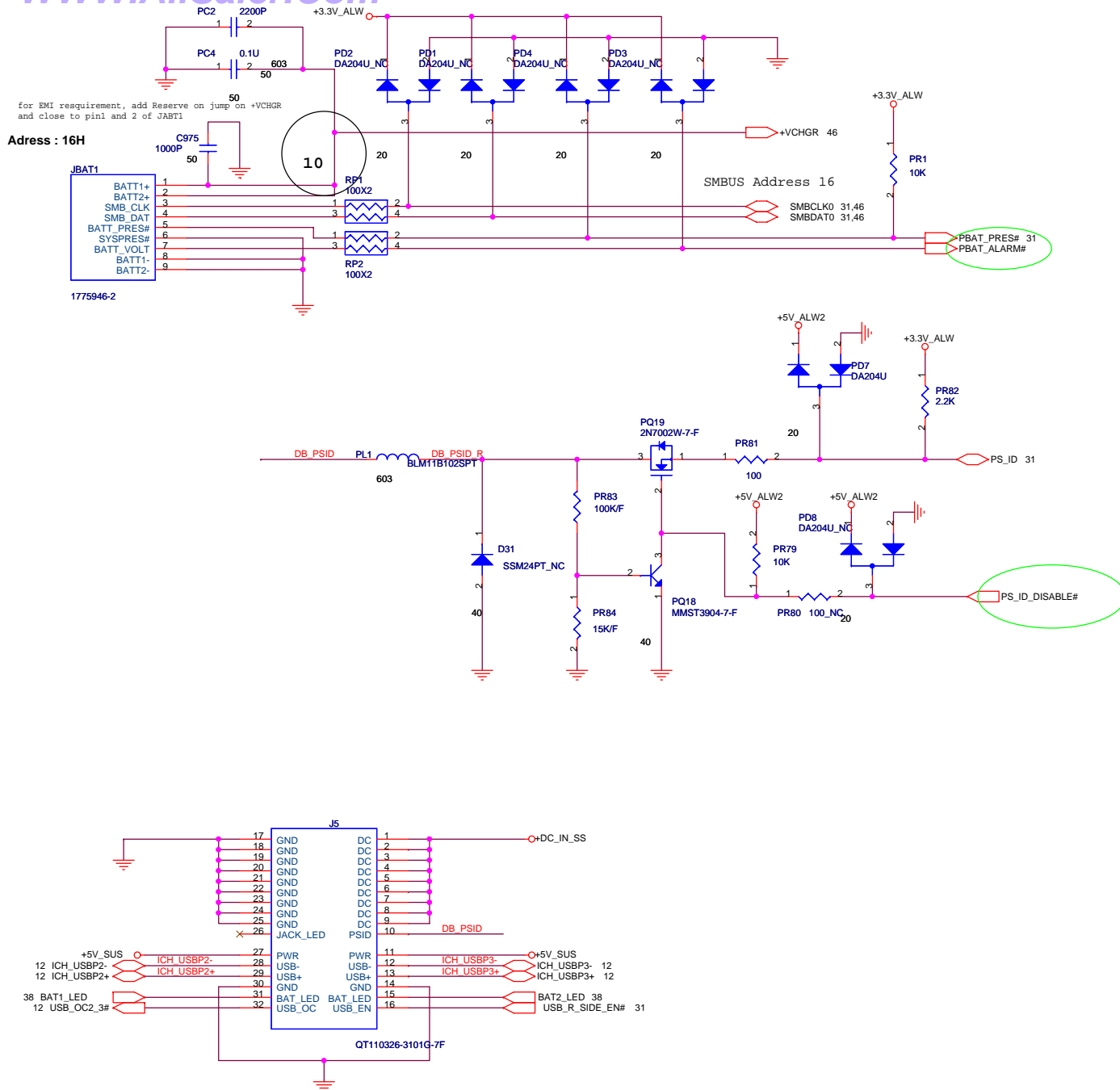
Reserve discharge path



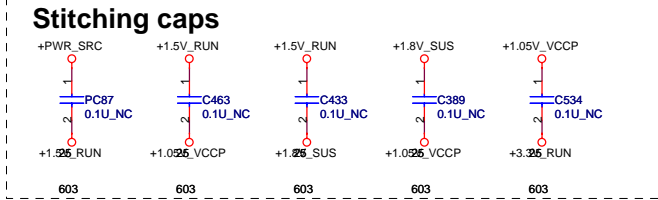
Reserve discharge path



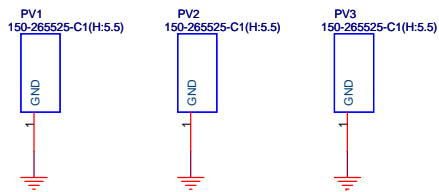
Title			RUN POWER SW
Size	Document Number	Rev	
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```
8/15: move the right side USB and DC-in connector schematic to DB.
so change BTB(J14) CONN to 32pin
```



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Page 26
SATA (HDD&CD_ROM)

Page 27
PCCARD /CONN

Page 31
SIO(MEC5025)

Page 38
Azelia CODEC

Page 40
LAN(BCM5755M)

Page 48
1.5VRUN,1.05V(VTT)

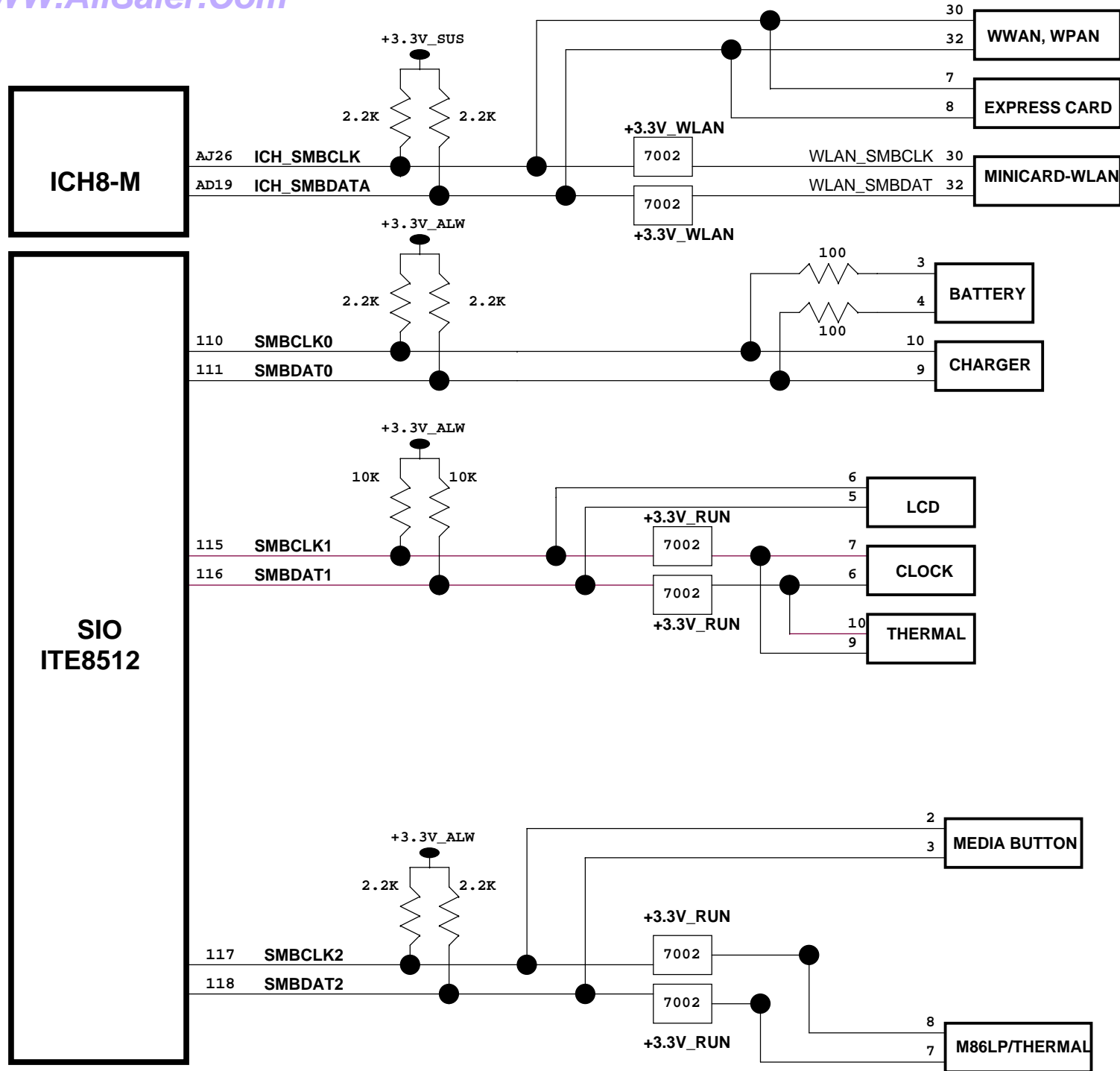
Place C860,C216,C1426 close to PQ33.
Place C862,C222,C1427 close to PQ73.

Page 49
1.25V,1.8V,0.9V

Place C867,C254,C1428 close to PQ91.
Place C863,C253,C1429 close to PQ92.

Page 51
CPU_MAX8786(3phase)

Page 52
D/D Power



Signal State	SLP S3#	SLP S4#	SLP S5#	S4 STATE#	ALWAYS PLANE	SUS PLANE	RUN PLANE	CLOCKS
S0 (Full ON) / M0	HIGH	HIGH	HIGH					
S3 (Suspend to RAM) / M1	LOW	HIGH	HIGH					
S4 (Suspend to DISK) / M1	LOW	HIGH	HIGH					
S5 (SOFT OFF) / M1	LOW	HIGH	LOW					
S3 (Suspend to RAM) / M-OFF	LOW	HIGH	HIGH					
S4 (Suspend to DISK) / M-OFF	LOW	LOW	HIGH					
S5 (SOFT OFF) / M-OFF	LOW	LOW	LOW					

PM TABLE

power plane State	+3.3V_ALW +3.3V_RTC_LDO +3.3V_WLAN +5V_ALW +15V_ALW	+1.8V_SUS +1.8V_LOM +3.3V_LAN +3.3V_SUS +5V_SUS	+0.9V_DDR_VTT +1.05V_VCCP +1.25V_RUN +1.5V_CARD +1.5V_RUN +3.3V_CARD +3.3V_CARDAUX +3.3V_R5C832 +3.3V_RUN	+3.3V_RUN_CARD +2.5V_RUN +5V_MOD +5V_RUN +5V_SPK_AMP +CPU_PWR_SRC +VCC_CORE +VDDA	+DC_IN +DC_IN_SS +PWR_SRC +RTC_CELL
S0	ON	ON	ON		ON
S3	ON	ON	OFF		ON
S5 S4/AC	ON	OFF	OFF		ON
S5 S4/AC don't exist	OFF	OFF	OFF		ON

PCI TABLE

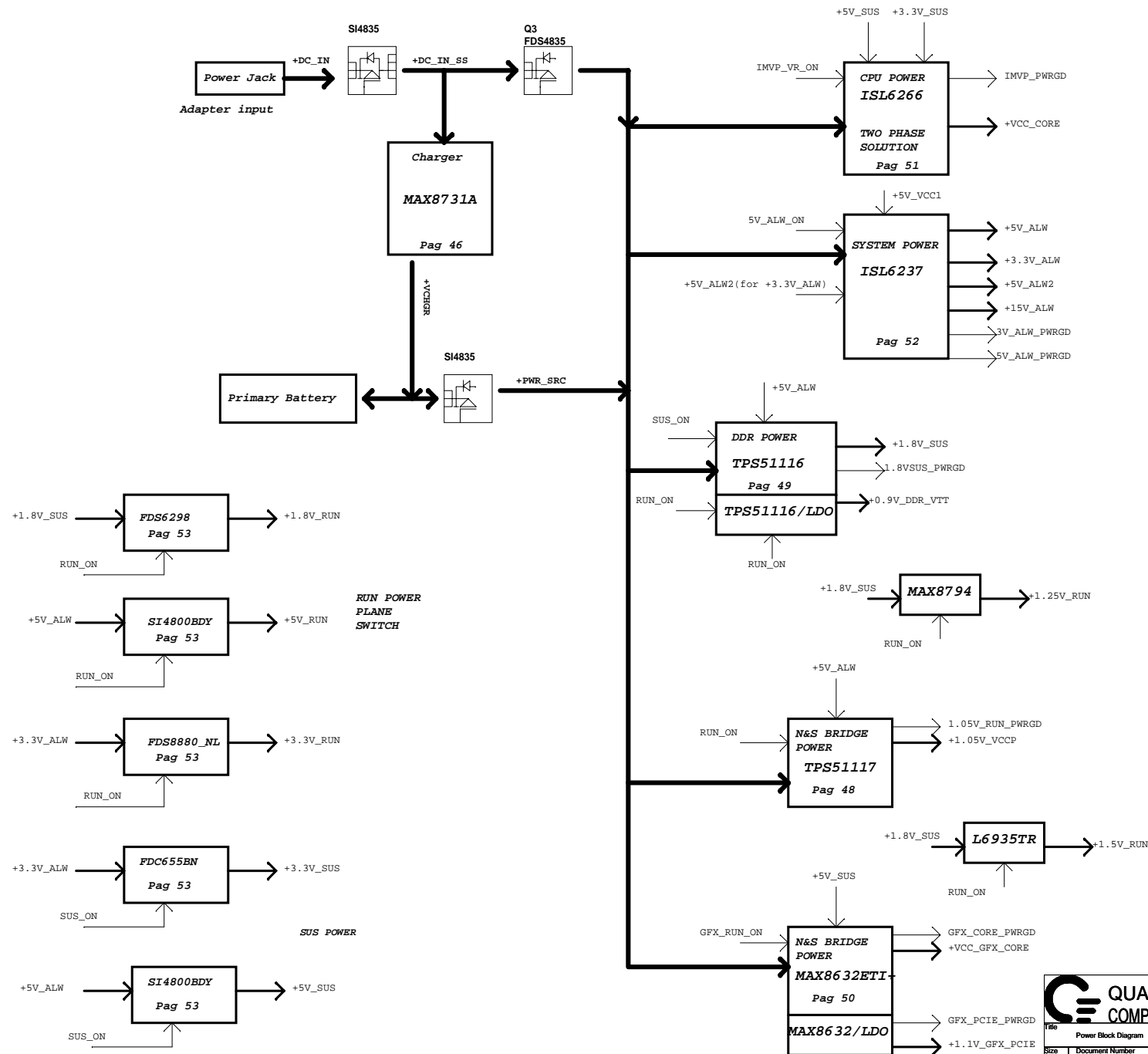
PCI DEVICE	IDSEL	REQ#/GNT#	PIRQ
BCM4401B	AD16	REQ#0 / GNT#0	PIRQB
R5C833	AD17	REQ#1 / GNT#1	PIRQC: Card reader PIEQD: 1394


	USB PORT#	DESTINATION
ICH8-M	0	Right Top
	1	Right Bottom
	2	Side TOP
	3	Side Bottom
	4	Ext. USB TOP
	5	Digital Camera
	6	Express Card
	7	WPAN/Bluetooth
	8	Ext. USB Bottom
ECE 5011	9	WWAN
	1	None
	2	None
	3	None
	4	None


PCI EXPRESS	DESTINATION
Lane 1	MINI CARD-1 WWAN
Lane 2	MINI CARD-2 WLAN
Lane 3	MINI CARD-3 WPAN
Lane 4	Express Card
Lane 5	None
Lane 6	None


GM3 Power Design Block Diagram

2007/09/06




		QUANTA COMPUTER	
KIO exchange Inc			
area	Customer Number		726
	0000		

		QUANTA COMPUTER	
2011 changes list			
ELECTRONIC PRODUCT		2011	
GARS			
Monday, March 14, 2011			

Model	Item	Page	Date	Rev.	Description
Pacino of Intel	1	32	11/26	2B	Change RTC connector because ME modifyr.
	2	31	11/26	2B	Exchange 'SNIFFER_PWR_SW#' AND 'WIRELESS_ON/OFF#. per EC limitation.
	3	31	11/26	2B	Change NUM_LED# from SIO pin98 to pin 88 and used Pin 98 for BID only per EC limitation.
	4	54	11/26	2B	Change PSID relation parts to +5V_ALW2 for power saving in S5.
	5	38	11/26	2B	Change Sniffer Switch power rail from RUN plane to ALW plane.
	6	43	11/26	2B	Added LINK1000# for BCM cann't support GLAN LED driven by LINKLED#/SPD100LED#.
	7	45	11/27	2B	Modify Screw hole base on ME update.
	8	17	11/29	2B	Link to MCH DPLL clock is wrong. Change to correct link.
	9	31	11/30	2B	Fine tune GPIO define for EC.
	10	37	12/04	2B	Change MMB LED power source from 5V_ALW2 plane to 5V_ALW for power saving and avoid LED flash when AC in.
	11	22	12/04	2B	Check AMD +3.3V_DELAY power plane connection component for AMD new update REF133-7 file.
	12	40	12/19	2B	Change Audio AMP thermal PAD leave to NC.
	13	31	12/26	2B	Change SMBus pull hihg resistor form 2.2k to 10k for LED panel flash.
	14	37	12/26	2B	since we will use WLAN and BT LED to show function at factory side. Change power supply of Cap and Num LED from 5V_ALW2, 3.3V_ALW to 5V_RUN and 3.3V_RUN.
	15	19	12/26	2B	Change HDMI detect circuit to solve external panel feed back voltage shortage then caude ATI chip can't switch to HMDI mode problem.
	16	37	12/26	2B	Change the Media board power from 3V_ALW to 5V_ALW2 to solve LED flash issue when AC/Bat plug in.
	17	37	12/26	2B	Change the lid switch IC power source from 3.3V_SUS to 3.3V_ALW to avoid system can enter S4 mode but wake up fail problem
	18	48	1/3	2B	Change PC85 to 680P for meet sequence.
	19	50	1/3	2B	Change PR7 to 4.99K for adjust +1.1V_GFX_PCIE rail.
	20	53	1/3	2B	Change PQ11 from S08 to power package footprint.
	21	48 49 50 52	1/3	2B	Change PR161 ,PR172 ,PR11 ,PR114 to correct resistance for reliability request.
	22	35	1/4	2B	remove USB charge circuit
	23	26	1/7	2B	pull DPST signal to high for setting 100% duty cycle
	24	31	1/7	2B	pin12 should reserve 1u cap for ITE8512JX using
	25	19	1/7	2B	modify HDMI detect circuit to fix the monitor detection problem..
	26	55	1/7	2B	create EMI spring
	27	31	1/11	2B	per TXC report, we should change W1 cap to 18p
	28	41	1/11	2B	per IDT FAE suggestion, serial 22 ohm on DMIC_CLK can help DMIC performance
	29	37	1/11	2B	add 10u cap at JMB1, let 3.3V_ALE get lower drop voltage on MMB side.
	30	6, 19	1/11	2B	EMI demand add 33p cap on RGB signal.
					<div>  <div> <div>QUANTA</div> <div>COMPUTER</div> </div> </div> <div> <div>Title</div> <div>X02 change list</div> </div> <div> <div>Size</div> <div>Document Number</div> <div>Rev</div> <div>2B</div> </div>

Model	Item	Page	Date	Rev.	Description
Pacino of Intel	1	25	2/14	3A	add level shift to separate the data and CLK of VGA IC and HDMI TV, and also reduce stray capacitance.
	2	25	2/14	3A	change diode to reduce stray capacitance per WPI suggestion
	3	12,28,31	2/14	3A	use pin-22 monitor ICH_AZ_CODEEC_RST# to delay NB_MUTE# signal for solve PO noise issue
	4	50	2/20	3A	For Reliability calculate , change PR11 from 150K to 178K.
	5	51	2/20	3A	Due to C4E hung up issue, change v_core power IC from ISL6266A to ISL6262A. Below is change list. 1. PU2: Change PN from AL006266000 to AL006262025 2. PR24: Change PN from CS28252FB15 to CS26812FB13 3. PC39: Change PN from CH11006JB18 to CH12206KB14 4. PC38: Change PN from CH12704JB07 to CH14706KB18 5. PR139: Change PN from CS11002JB32 to CS12552FB18 6. PC141: Change PN from CH22206KB16 to CH21006JB10 7. PC40,PC41: Change PN from CH1336K1B02 to CH31006KB18 8. PR136: Change PN from CS23833F911 to CS24533F921
	6	48	2/20	3A	For 1.05V OVP issue in Vista , no stuff PC62.
	7	25	2/20	3A	Due to L6935 has improved powergood issue, no stuff PR183 and stuff PR66.
	8	48	2/15	3A	add HDMI solution per Silicon image suggestion 1. Change R233 to 650 ohm 2. Remove external RC between HDMI +/- signal. add HDMI EMI solution DIS:CXCG900U000 / EXC24CG900U; UMACXCG240U000 / EXC24CG240U
	9	35	2/23	3A	add common chock for EMI solution Quanta PN: DC09004A014
	10	35	2/25	3A	cange power jump to 0805 resistor
	11	27	2/25	3A	add filter CAP for EMI
	12	13, 37	2/25	3A	by ICH-8 GPIO-17 detect the LED keyboard connector
	13	17	2/26	3A	exchange 27SS and 27NSS / DREF_SSCLK# & DREF_SSCLK for follow CLK GEN spec. design.
	14	13	2/27	3A	ICH_RSMRST# pull down for RTC timer issue when plug in AC
	15	40, 41	2/27	3A	MUST ADD 2.2K-OHM RESISTORS TO PREVENT AMPLIFIER CLIPPING and ADD 220PF CAPACITORS TO ALLOW PROPER DYNAMIC RANGE MEASUREMENTS
	16	19	22/29	3A	Add 10k ohm on HDMI_DET to ensure Vin on test fixture input 2.4V the voltage not drop under 2V spec. definition.
	17	9	03/03	3A	Based on SR_check 1.6, UMA should pull down 75 ohm on TV_DAC pins if disable TV-out function.
	18	40	03/05	3A	Change C518, C519 from 0.033uF to 0.01uF per Dell audio update requirement.
	19	40	03/20	3A	Change net name of "AUD_HP2_L1" between R708 & C525 to "AUD_HP2_L0_R" and "AUD_HP2_R1" between R708 & C525 to "AUD_HP2_R0_R" for the original net name same as U20.15 & U20.16 will cause the HP2 no function.



QUANTA

COMPUTER

Title			A00 change list
Size	Document Number	Rev	
GM3		2B	
<div> <div> <div>2008</div> <div>25</div> <div>2008</div> </div> <div>16</div> <div>2008</div> </div>			